

General Description

The LTA6321, LTA6358 and LTA6324 are a family of micro-power, 36 V wide supply voltage, rail-to-rail output operational amplifiers capable of operating on supplies ranging from +4 V to +36 V. LTA6321/LTA6358/LTA6324 offer outstanding dc precision and ac performance, including low offset (± 2 mV typically), low offset drift ($\pm 5 \mu\text{V}/^\circ\text{C}$ typically), 1.1 MHz bandwidth, Unique features such as differential input-voltage range to the negative supply rail, Short-circuit current (± 35 mA), high capacitive load drive of up to 1 nF, and high slew rate ($0.8 \text{ V}/\mu\text{s}$) make the LTA6321/LTA6358/LTA6324 high-performance operational amplifiers for high-voltage industrial applications.

The robust design of the LTA6321/LTA6358/LTA6324 family provides ease-of-use to the circuit designer: integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection. The LTA6321/LTA6358/LTA6324 are optimized for operation at voltages from +4V ($\pm 2\text{V}$) to +36 V ($\pm 18 \text{ V}$) over the extended temperature range of -40°C to $+125^\circ\text{C}$.

Features and Benefits

- Wide Supply: $\pm 2 \text{ V}$ to $\pm 18 \text{ V}$, 4V to 36 V
- Low Offset Voltage: ± 2 mV typically
- Low Offset Voltage Drift: $\pm 5 \mu\text{V}/^\circ\text{C}$
- High Common-Mode Rejection: 110 dB
- Gain Bandwidth: 1.1 MHz
- Slew Rate: $0.8 \text{ V}/\mu\text{s}$
- Low Noise: 53 nV/ $\sqrt{\text{Hz}}$ at 1 kHz
- Low Quiescent Current: 260 μA per amplifier
- Rail-to-Rail Output

Applications

- Tracking Amplifier in Power Modules
- Power Delivery: UPS, Server, and Merchant Network Power
- High-Side and Low-Side Current Sensing
- Transducer Amplifiers
- Battery-Powered Instruments
- Test and Measurement Equipment
- Multiplexed Data-Acquisition Systems
- Programmable Logic Controllers

Pin Configuration (Top View)

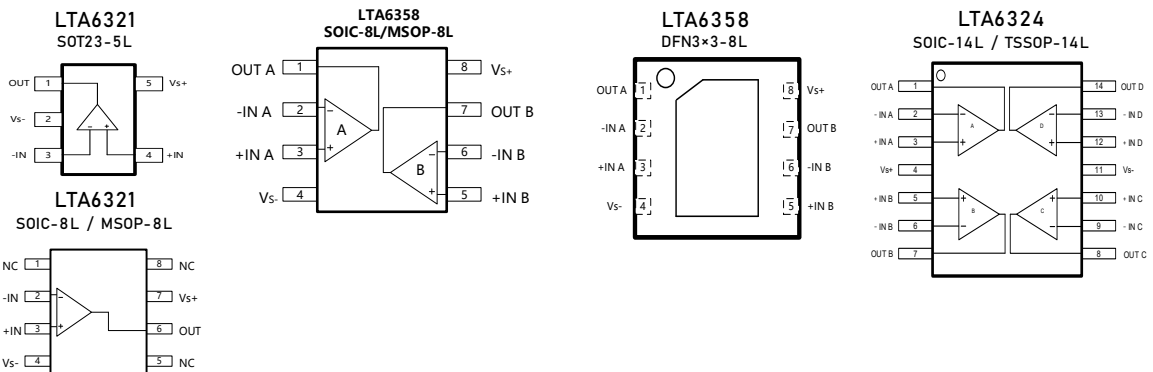


Table of Content

General Description	1
Features and Benefits	1
Application	1
Pin Configuration (Top View)	1
Table of Content	2
Pin Description.....	3
Ordering Information ⁽¹⁾	3
Tape and Reel Information	4
Package Outlines	5
Important Notice	11

preliminary

Pin Description

Symbol	Description
-IN	Inverting input of the amplifier. The voltage range is from V_{S-} to $V_{S+} - 1.5V$.
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as -IN.
V_{S+}	Positive power supply. The voltage is from 4V to 36V. Split supplies are possible as long as the voltage between V_{S+} and V_{S-} is from 4V to 36V.
V_{S-}	Negative power supply. It is normally tied to ground. It can also be tied to a voltage other than ground as long as the voltage between V_{S+} and V_{S-} is from 4V to 36V.
OUT	Amplifier output.
NC	No connection

Ordering Information ⁽¹⁾

Type Number	Package Name	Package Quantity	Eco Class ⁽²⁾	Marking Code ⁽³⁾
LTA6321XT5/R6	SOT23-5L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	F21
LTA6321XS8/R8	SOIC-8L	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	F6321
LTA6321XV8/R6	MSOP-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	F6321
LTA6358XS8/R8	SOIC-8L	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	F6358
LTA6358XV8/R6	MSOP-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	F6358
LTA6358XF8/R6	DFN3x3-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	F6358
LTA6324XS14/R5	SOIC-14L	Tape and Reel, 2 500	Green (RoHS & no Sb/Br)	F6324
LTA6324XT14/R6	TSSOP-14L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	F6324

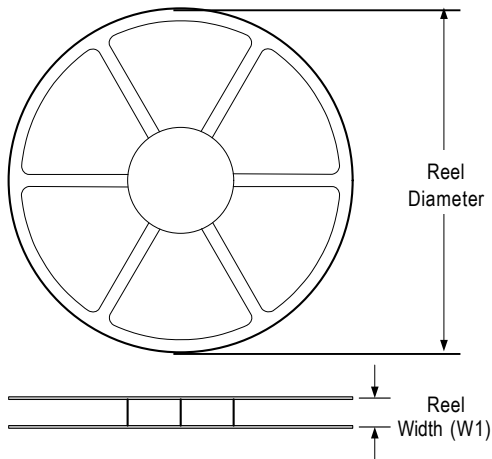
(1) Please contact to your Linearin representative for the latest availability information and product content details.

(2) Eco Class - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & Halogen Free).

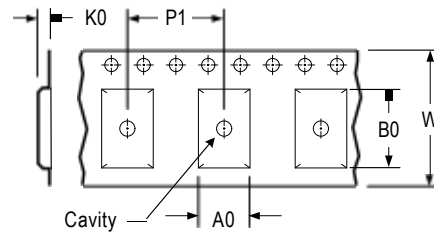
(3) There may be multiple device markings, a varied marking character of "x", or additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

Tape and Reel Information

REEL DIMENSIONS

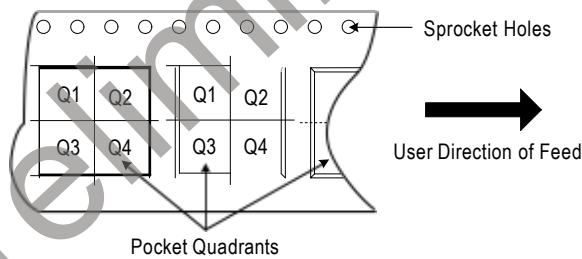


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

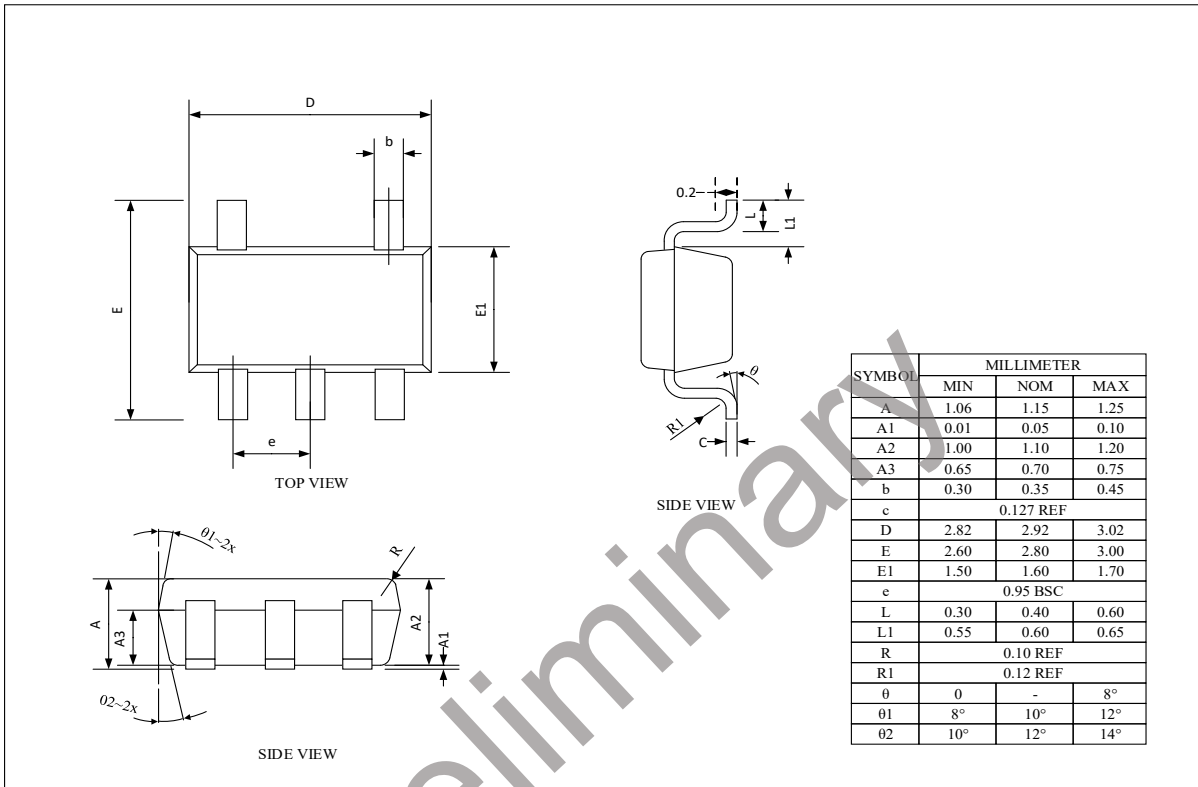


* All dimensions are nominal

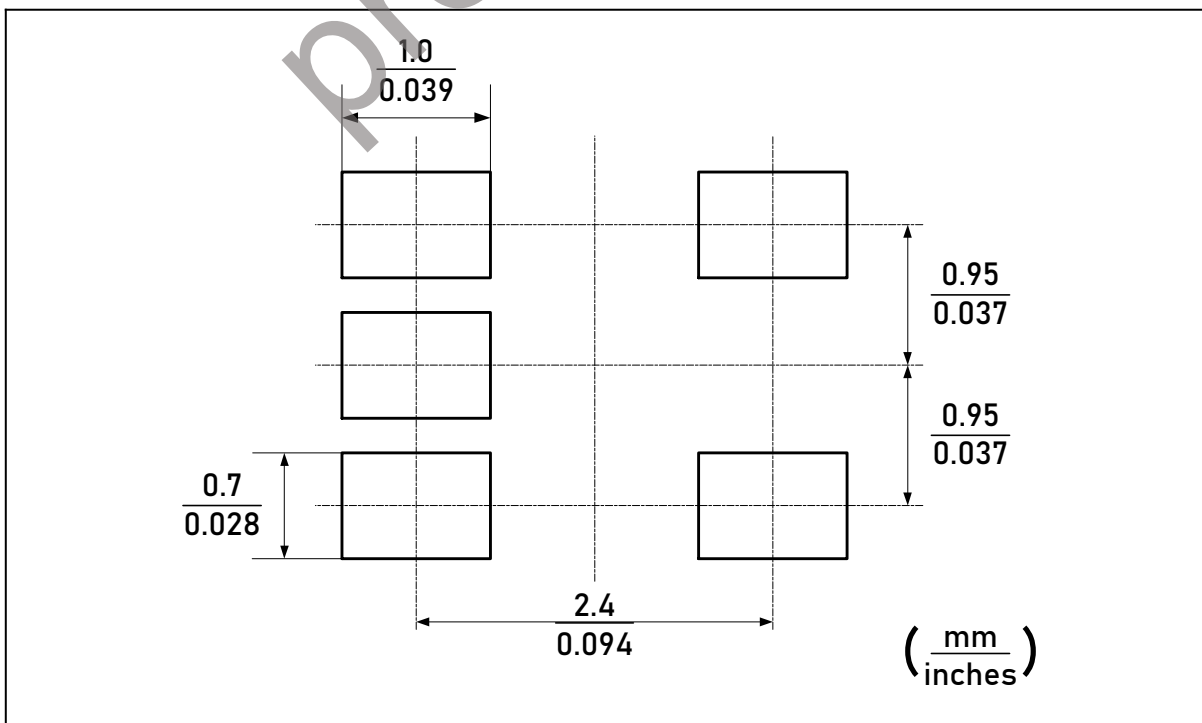
Device	Package Type	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
LTA6321XT5/R6	SOT23	5	3 000	178	9.0	3.3	3.2	1.5	4.0	8.0	Q3
LTA6321XS8/R8	SOIC	8	4 000	330	12.5	6.6	5.3	2.0	8.0	12.0	Q1
LTA6321XV8/R6	MSOP	8	3 000	330	12.5	5.0	3.5	2.0	8.0	12.0	Q1
LTA6358XV8/R6	MSOP	8	3 000	330	12.5	5.0	3.5	2.0	8.0	12.0	Q1
LTA6358XS8/R8	SOIC	8	4 000	330	12.5	6.6	5.3	2.0	8.0	12.0	Q1
LTA6358XF8/R6	DFN3×3	8	3 000	330	12.5	3.3	3.3	1.1	8.0	12.0	Q1
LTA6324XS14/R5	SOIC	14	2 500	330	12.5	6.5	9.5	2.0	8.0	16.0	Q1
LTA6324XT14/R6	TSSOP	14	3 000	330	12.5	6.9	5.5	1.2	8.0	16.0	Q1

Package Outlines

DIMENSIONS, SOT23-5L

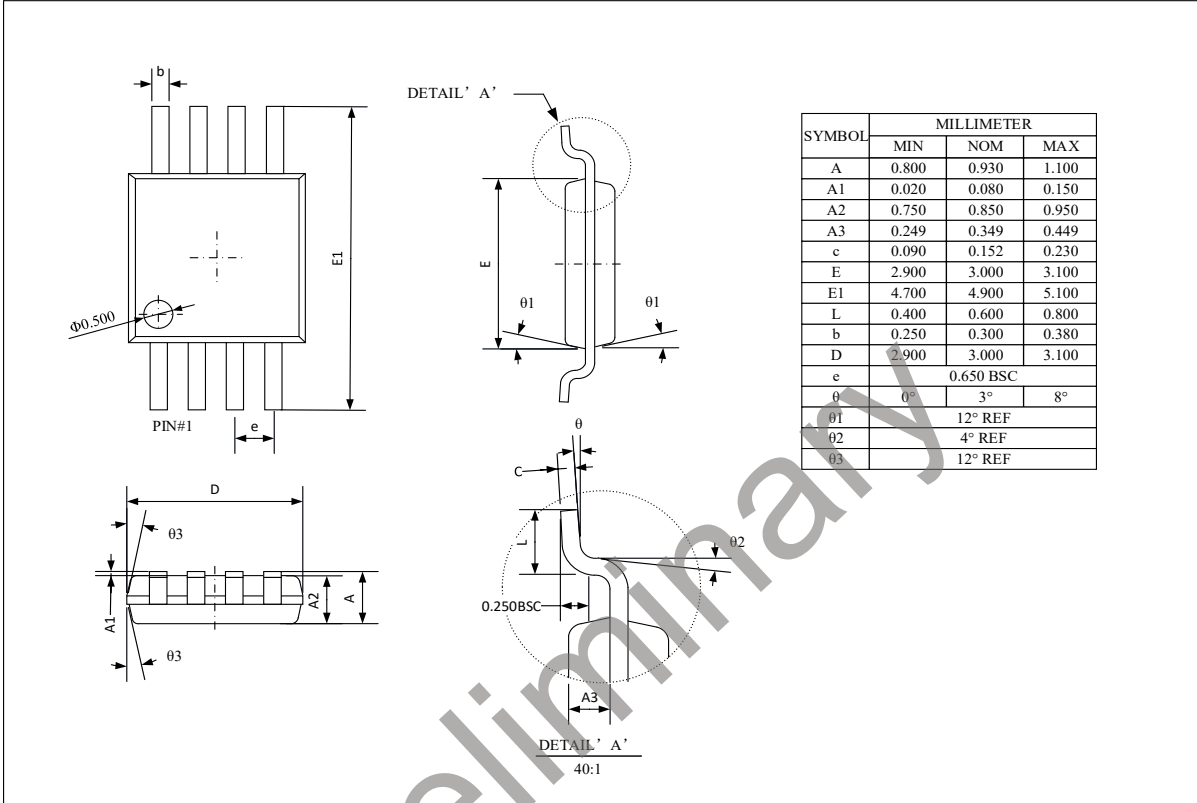


RECOMMENDED SOLDERING FOOTPRINT, SOT23-5L

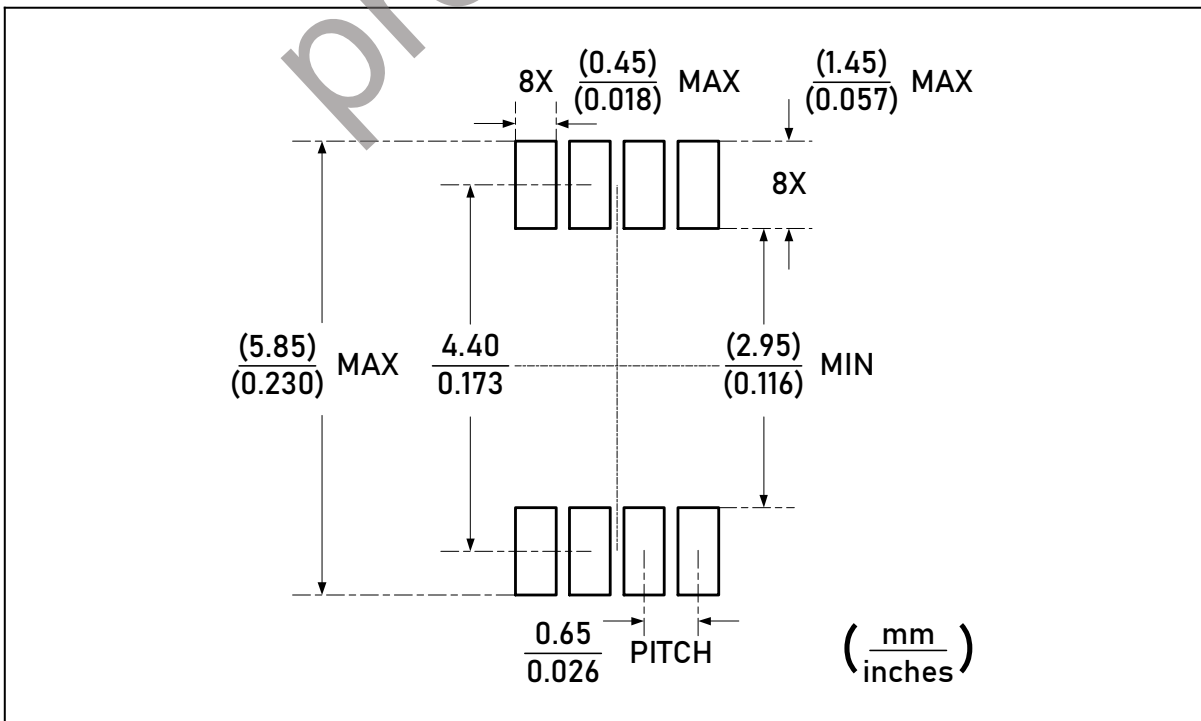


Package Outlines (continued)

DIMENSIONS, MSOP-8L



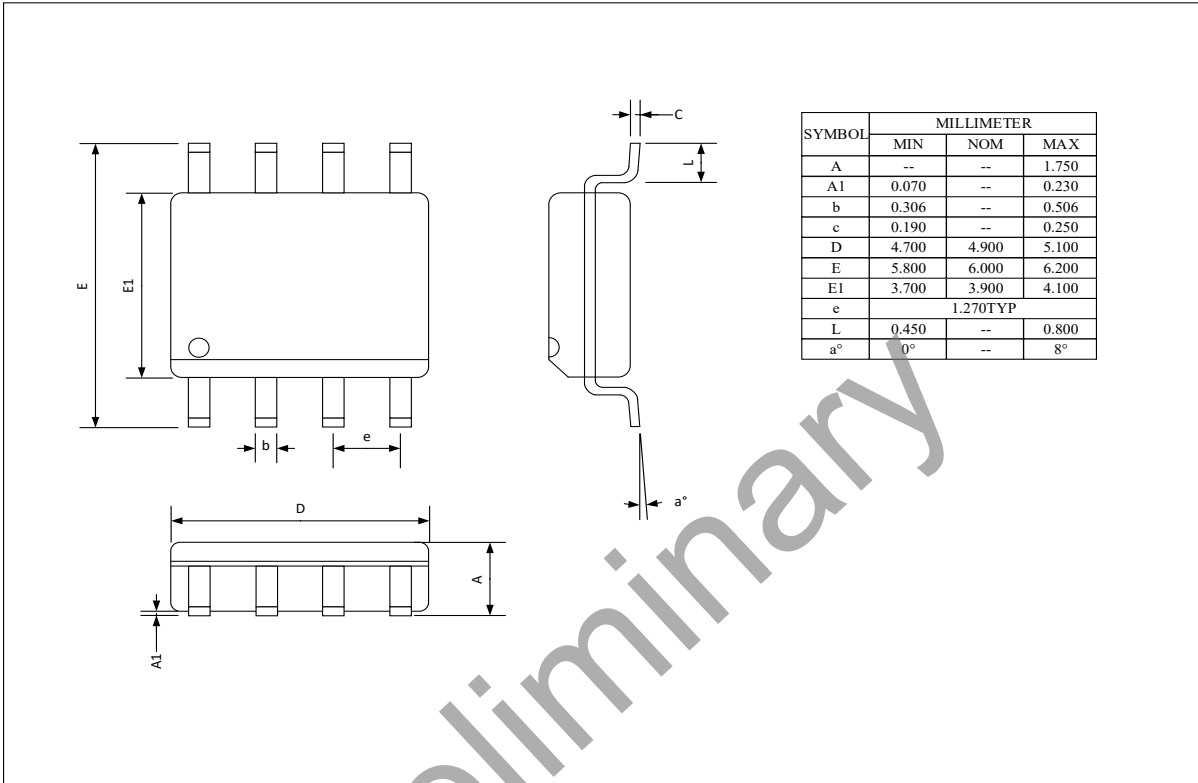
RECOMMENDED SOLDERING FOOTPRINT, MSOP-8L



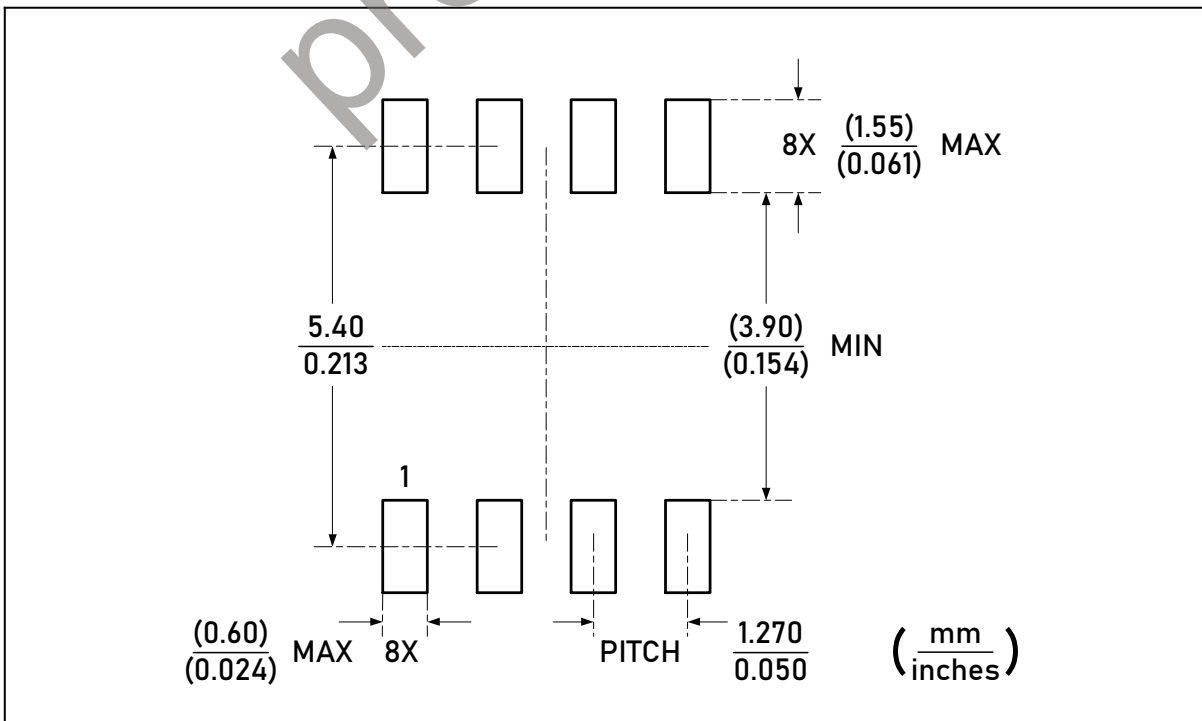
CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures.
Linearin and designs are registered trademarks of Linearin Technology Corporation.
© Copyright Linearin Technology Corporation. All Rights Reserved.
All other trademarks mentioned are the property of their respective owners.

Package Outlines (continued)

DIMENSIONS, SOIC-8L



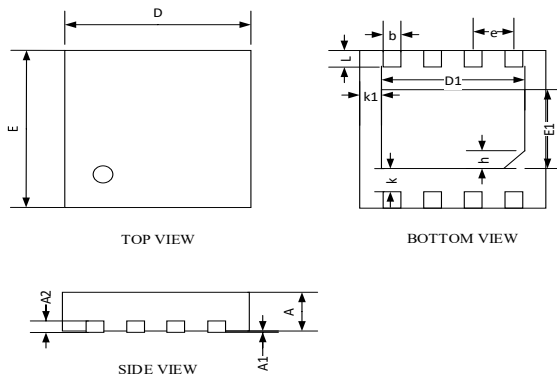
RECOMMENDED SOLDERING FOOTPRINT, SOIC-8L



CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures.
Linearin and designs are registered trademarks of Linearin Technology Corporation.
© Copyright Linearin Technology Corporation. All Rights Reserved.
All other trademarks mentioned are the property of their respective owners.

Package Outlines (continued)

DIMENSIONS, DFN3x3-8L

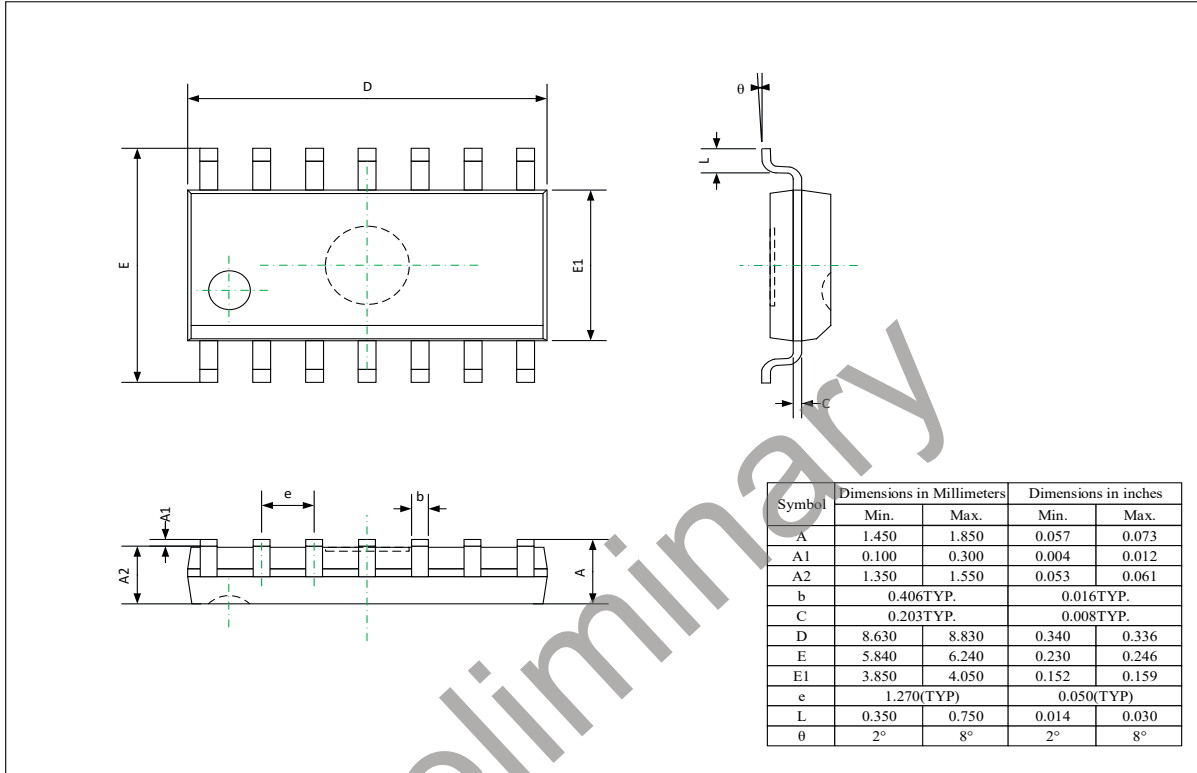


SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
b	0.25	0.28	0.31
A2	0.21 BSC		
D	2.90	3.00	3.10
E	2.90	3.00	3.10
E1	1.45	1.50	1.55
D1	2.25	2.30	2.35
c	0.65 REF		
L	0.25	0.30	0.35
h	0.49 REF		
k1	0.30	0.35	0.40
k	0.40	0.45	0.50

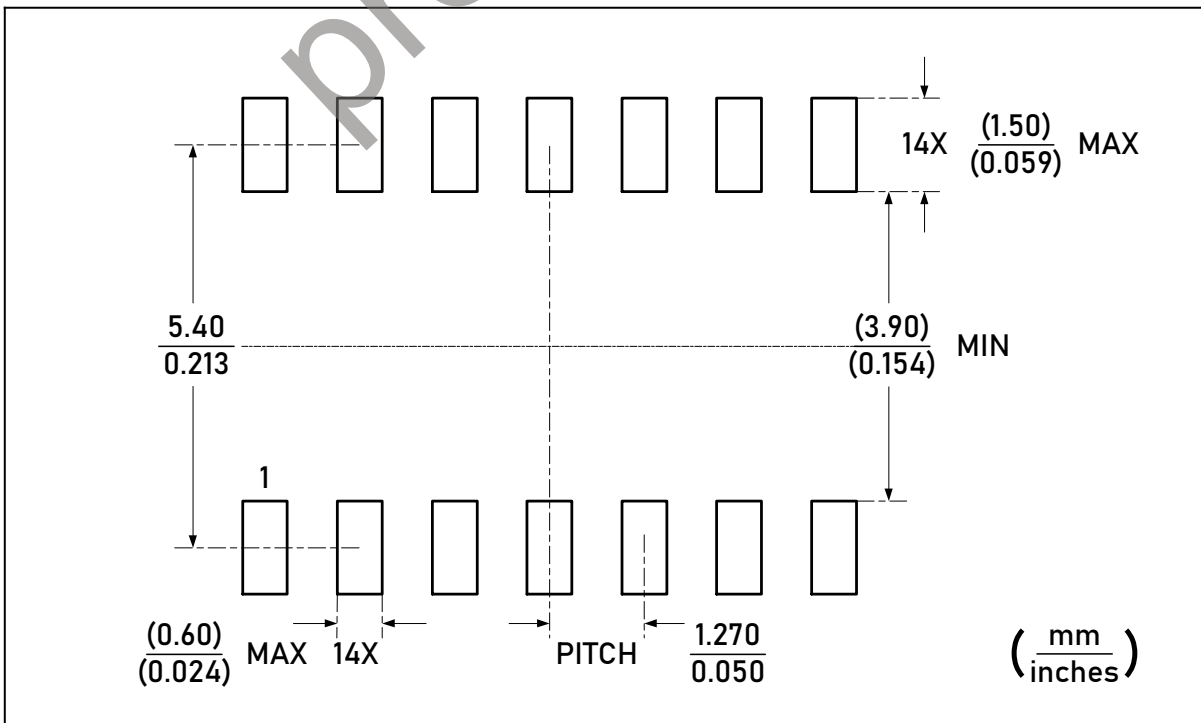
preliminary

Package Outlines (continued)

DIMENSIONS, SOIC-14L



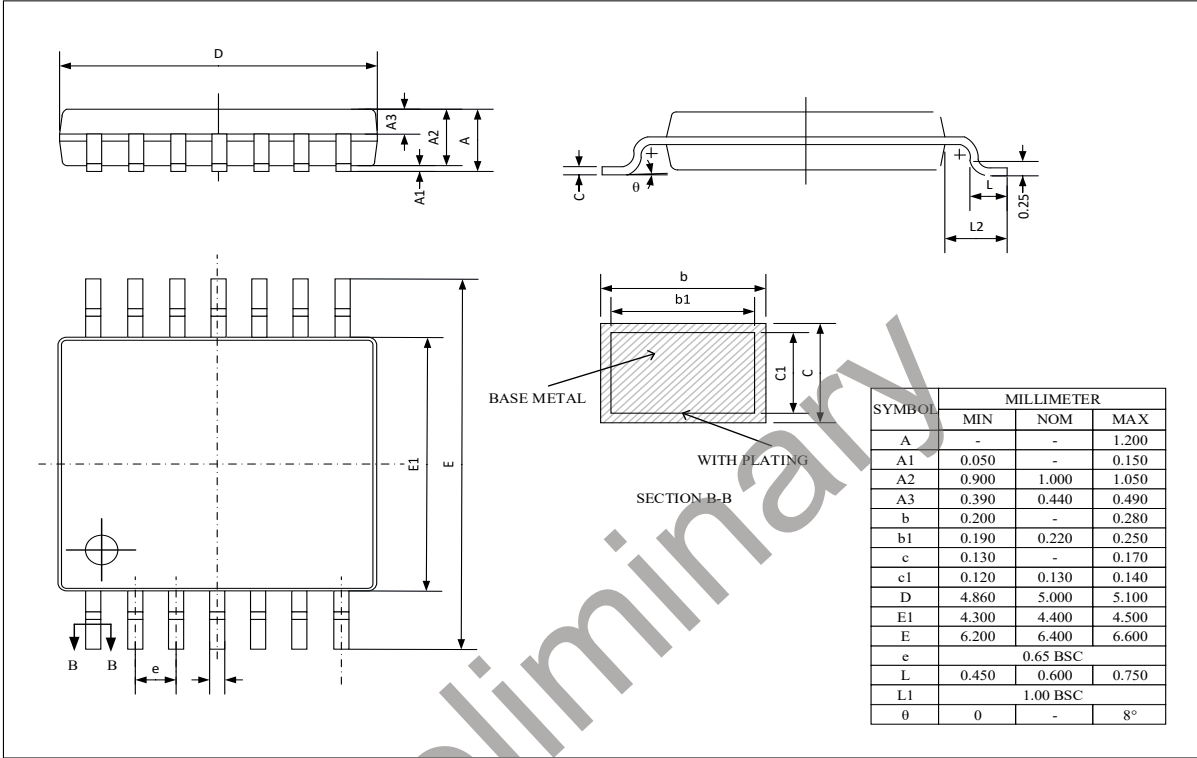
RECOMMENDED SOLDERING FOOTPRINT, SOIC-14L



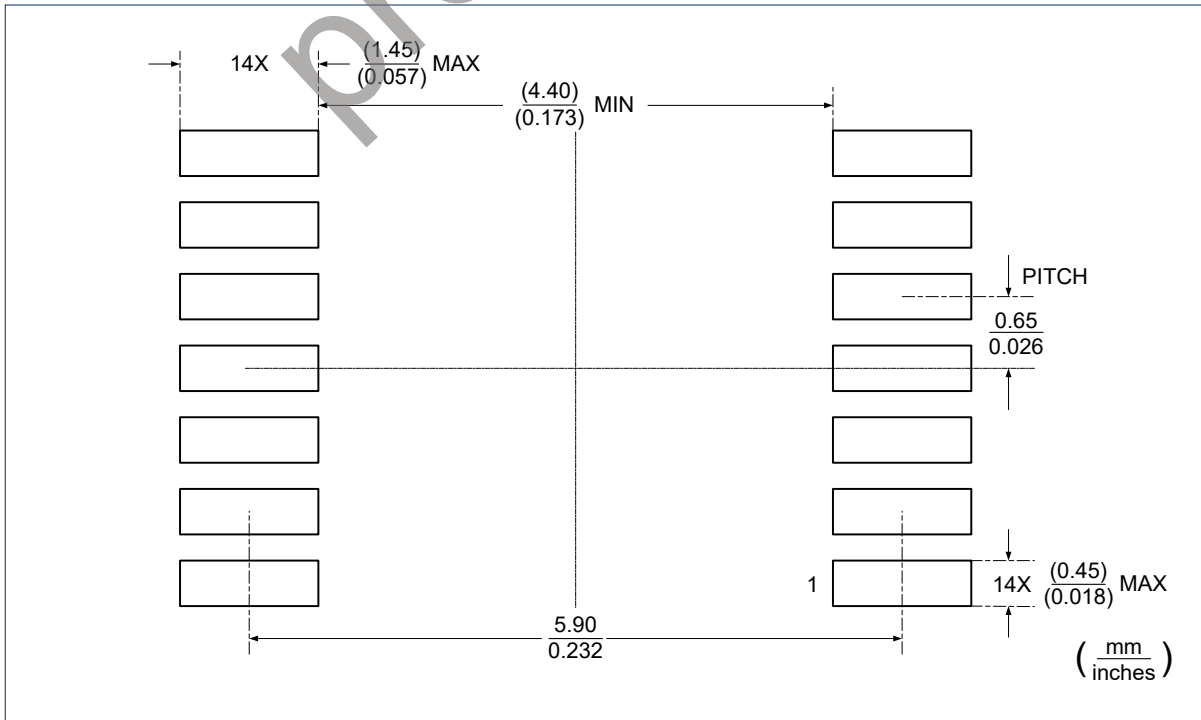
CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures.
 Linearin and designs are registered trademarks of Linearin Technology Corporation.
 © Copyright Linearin Technology Corporation. All Rights Reserved.
 All other trademarks mentioned are the property of their respective owners.

Package Outlines (continued)

DIMENSIONS, TSSOP-14L



RECOMMENDED SOLDERING FOOTPRINT, TSSOP-14L



CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures.
Linearin and designs are registered trademarks of Linearin Technology Corporation.
© Copyright Linearin Technology Corporation. All Rights Reserved.
All other trademarks mentioned are the property of their respective owners.

Important Notice

Linearin is a global fabless semiconductor company specializing in advanced high-performance high-quality analog/mixed-signal IC products and sensor solutions. The company is devoted to the innovation of high performance, analog-intensive sensor front-end products and modular sensor solutions, applied in multi-market of medical & wearable devices, smart home, sensing of IoT, intelligent industrial & smart factory (industrie 4.0), and automotives. Linearin's product families include widely-used standard catalog products, solution-based application specific standard products (ASSPs) and sensor modules that help customers achieve faster time-to-market products. Go to <http://www.linearin.com> for a complete list of Linearin product families.

For additional product information, or full datasheet, please contact with the Linearin's Sales Department or Representatives.

preliminary