

36V, High-precision, low-noise, rail-to-rail, zero-drift operational amplifier

General Description

The LTA8191 (single-channel) and LTA8192 (dual-channel) are high-voltage, high-precision, low-noise, fast-settling, rail-to-rail output, zero-drift operational amplifiers. The input common-mode voltage range includes the negative power supply rail. Additionally, these amplifiers offset only 1 μ V (typical) and an ultra-low temperature drift of 0.005 μ V/ $^{\circ}$ C.

It supports both single and dual power supply configurations. When powered by a single supply, the voltage range is +4.5V to +36V; with dual supplies, the voltage range is ± 2.25 V to ± 18 V.

The main package types of LTA8191 are SOT23-5L, MSOP-8L and SOIC-8L. For LTA8192, it could support SOIC-8L. All package types operate within a temperature range of -40° C to 125° C.

Features and Benefits

- Low offset voltage: 8 μ V maximum
- Zero Drift: 0.005 μ V/ $^{\circ}$ C
- Low noise: 9nV/ $\sqrt{\text{Hz}}$
- 0.1Hz to 10Hz Noise: 0.19 μ V_{P-P}
- High DC Accuracy:
 - Power Supply Rejection Ratio (PSRR): 138 dB
 - Common-mode rejection ratio (CMRR): 133 dB
- Gain-bandwidth product: 12MHz
- Quiescent current: 2.5 mA (typical)
- Operating power supply range: ± 2.25 V to ± 18 V
- Output rail-to-rail
- MUX-friendly inputs
- RFI/EMI filtered inputs
- The input can reach the negative power rail

Applications

- Bridge amplifier
- Sensor applications
- Temperature measurement
- Electronic scale
- Medical equipment
- Battery test
- Analog input module
- DC power supply, ac source, electronic load

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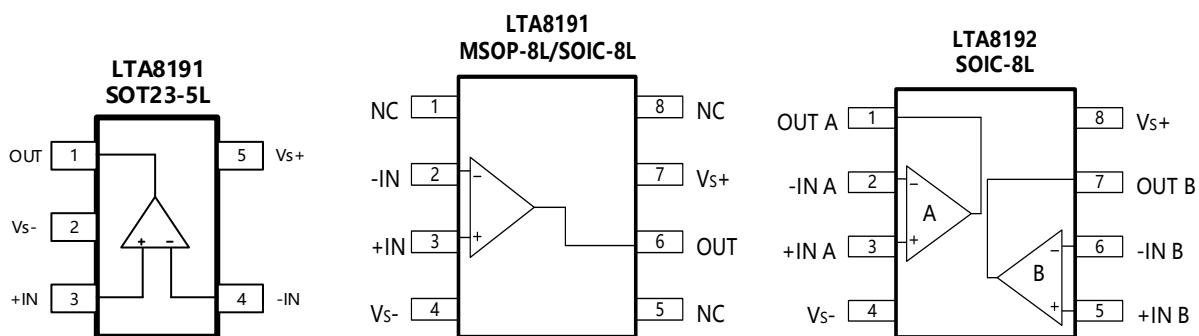
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Ordering Information

Part Number	Package Type	Package Size	Package Quantity	ECO Class	Mark Code
LTA8191XT5/R6	SOT23-5L	2.92 mm * 1.60 mm	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	Z91
LTA8191XS8/R8	SOIC-8L	4.90 mm * 3.92 mm	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	ZHV91
LTA8191XV8/R6	MSOP-8L	3.00 mm * 3.00 mm	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	ZHV91
LTA8192XS8/R8	SOIC-8L	4.90 mm * 3.92 mm	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	ZHV92

Pin Configuration (Top View)



PIN Name	Description
OUT A	Amplifier output A.
-IN A	Inverting input of the amplifier A.
+IN A	Non-inverting input of the amplifier A.
Vs-	Negative power supply. It is normally tied to ground. It can also be tied to a voltage other than ground as long as the voltage between Vs+ and Vs- is from 4.5 V to 36 V.
+IN B	Non-inverting input of the amplifier B.
-IN B	Inverting input of the amplifier B.
OUT B	Amplifier output B.
Vs+	Positive power supply. The voltage is from 4.5 V to 36 V. Split supplies are possible as long as the voltage between +Vs and -Vs is from 4.5 V to 36 V.
NC	No Connection

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Limiting Value

Parameter	Absolute Maximum Rating
Supply Voltage, V_{S+} to V_{S-}	40 V ($\pm 20V$)
Signal Input Terminals: Voltage	$V_{S-} - 0.3$ to $V_{S+} + 0.3$
Signal Input Terminals: Current	± 10 mA
Signal Differential Input Voltage	$V_{S+} - V_{S-} + 0.2$
Storage Temperature Range, T_{stg}	-65 °C to $+150$ °C
Junction Temperature, T_J	150 °C
Operation Temperature T_{JL}	-55 °C to $+150$ °C

Thermal Information

Thermal Metric	Package	Level	Unit
θ_{JA} Package Thermal Resistance	SOIC-8L	125	°C/W
	MSOP-8L	216	
	SOT23-5L	190	

ESD Ratings

Parameter	Item	Value	Unit
Electrostatic Discharge Voltage	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001	$\pm 4\ 000$	V
	Charged device model (CDM), per ANSI/ESDA/JEDEC JS-002	$\pm 1\ 000$	

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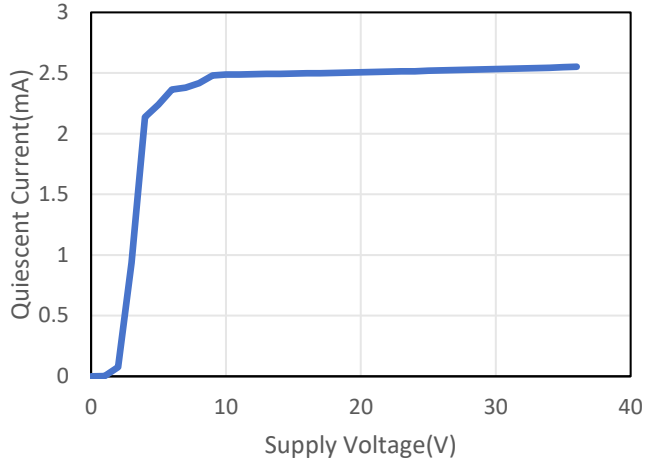
Electrical Characteristics

Unless otherwise noted, $T_A=25^\circ\text{C}$, $V_S = \pm 18.0\text{V}$, $V_{CM}=V_S/2$, $V_O=V_S/2$, $R_L=10\text{k}\Omega$, connect to $V_S/2$.

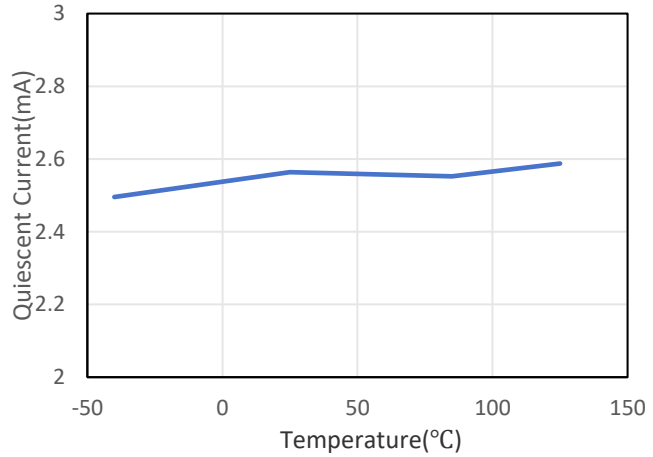
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
DC PERFORMANCE						
Input Offset Voltage	V_{OS}	$T_A=25^\circ\text{C}$		± 1	± 8	μV
		$T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$		± 1	± 14	μV
Input Offset Voltage vs Temperature	dV_{OS} / dT	$T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$		0.005		$\mu\text{V}/^\circ\text{C}$
Input Bias Current	I_B	$V_{CM} = V_S/2$		± 0.13	± 0.3	nA
		$T_A = 0^\circ\text{C}$ to $+85^\circ\text{C}$			± 1.5	
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$			± 5	
Input Offset Current	I_{OS}			± 200		pA
Large voltage gain	A_{VOL}	$V_{S-}+0.5\text{V} < V_O < V_{S+}-0.5\text{V}$		161		dB
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$		157		
NOISE and DISTORTION PERFORMANCE						
Input voltage noise	V_n	$f = 0.1\text{ Hz to }10\text{ Hz}$		0.19		μV_{P-P}
Input Voltage Noise Density	e_n	$f = 1\text{ kHz}$		9		$\text{nV}/\sqrt{\text{Hz}}$
INPUT CHARACTERISTICS						
Input Common Mode Voltage Range	V_{CM}	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	$V_{S-}-0.1$		$V_{S+}-2.6$	V
Common Mode Rejection Rate	CMRR	$V_S=36\text{V}$, $V_{CM}=0$ to 34.5V	131	150		dB
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	128			
DYNAMIC PERFORMANCE						
Unity-gain Bandwidth	UGB	$G = +1$		7.5		MHz
Gain-Bandwidth Product	GBP	$G = +100$		12		MHz
Slew Rate	SR	$G = +1$, 10V step		21		$\text{V}/\mu\text{s}$
Total harmonic distortion + noise	THD+N	$G = 1$, $f = 1\text{ kHz}$, $V_O = 3.5\text{ VRMS}$		0.0001%		
OUTPUT CHARACTERISTICS						
High Output Voltage Swing	V_{OH}	$R_L = 10\text{ k}\Omega$		$(V_{S+})-65$		mV
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$			$(V_{S+})-150$	
Low Output Voltage Swing	V_{OL}	$R_L = 10\text{ k}\Omega$		$(V_{S-})+200$		mV
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$			$(V_{S-})+350$	
Short-circuit Current	I_{source}			75		mA
	I_{sink}			85		
POWER SUPPLY						
Operating Supply Voltage	V_S	$T_A = -40$ to $+125^\circ\text{C}$	4.5		36	V
Power Supply Rejection Ratio	PSRR	$V_S = 4.5\text{V}$ to 36V , $T_A = 25^\circ\text{C}$	132	150		dB
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	127			
Quiescent Current per amplifier	I_q			2.5	3.6	mA
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$			3.8	

Typical Performance Characteristics

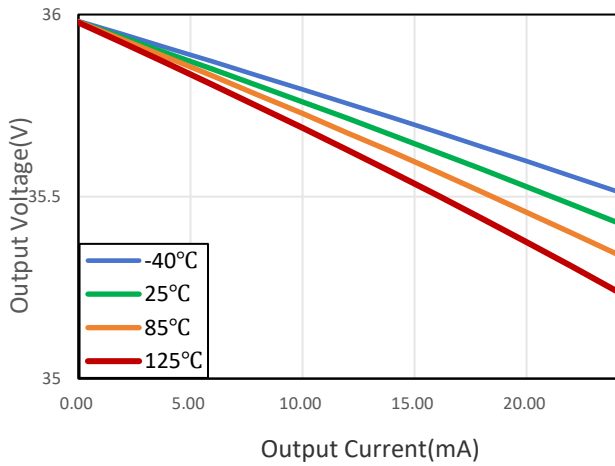
At $T_A = +25\text{ }^\circ\text{C}$, $V_S = \pm 18.0\text{ V}$, $V_{CM} = V_S / 2$, and $R_L = 10\text{ k}\Omega$ connected to $V_S / 2$, unless otherwise noted.



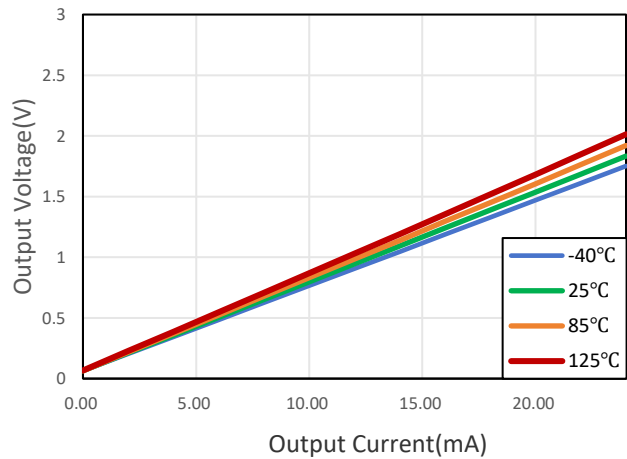
Quiescent Current vs. Supply Voltage



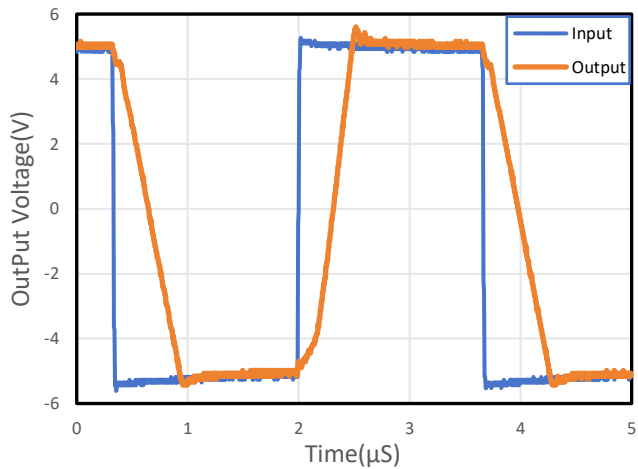
Quiescent Current vs. Temperature ($V_S=36\text{V}$)



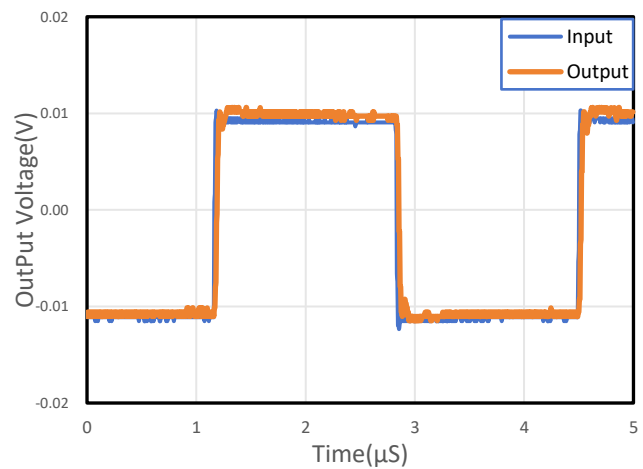
Output High Voltage Swing as a function of Output Current ($V_S=36\text{V}$)



Output Low Voltage Swing as a function of Output Current ($V_S=36\text{V}$)



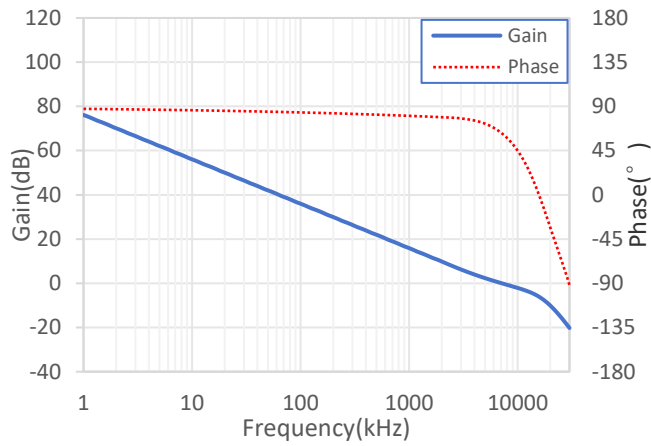
Large-Signal Step Response



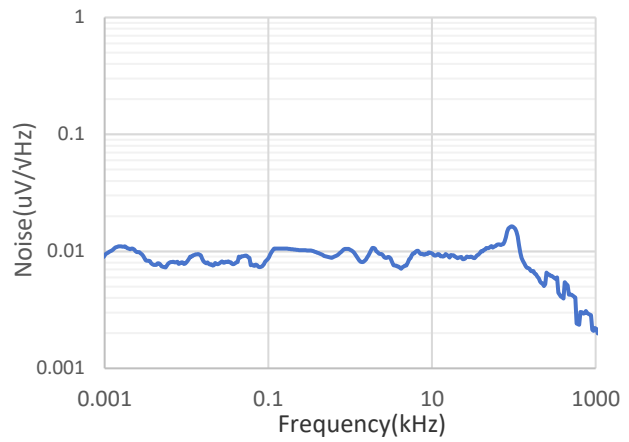
Small-Signal Step Response

Typical Performance Characteristics (Cont.)

At $T_A = +25\text{ }^\circ\text{C}$, $V_S = \pm 18.0\text{ V}$, $V_{CM} = V_S / 2$, and $R_L = 10\text{ k}\Omega$ connected to $V_S / 2$, unless otherwise noted.



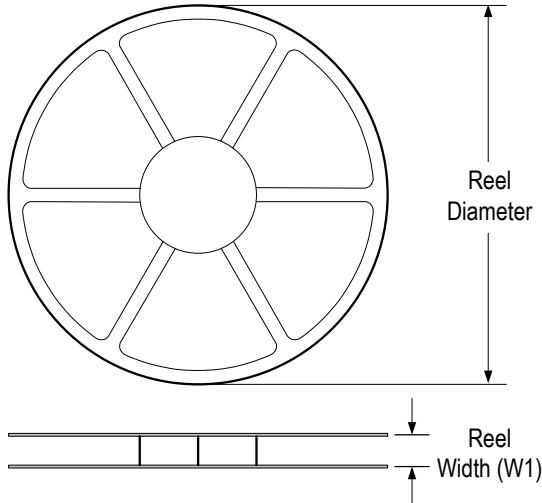
Open-Loop Gain and Phase vs. Frequency



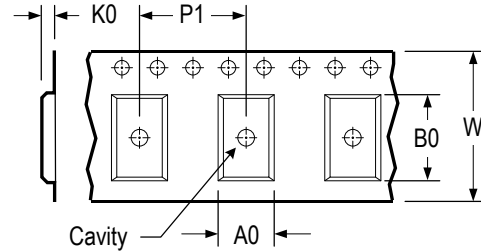
Voltage Noise Density vs. Frequency

Tape and Reel Information

REEL DIMENSIONS

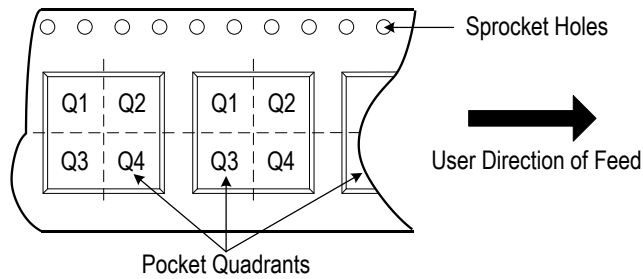


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

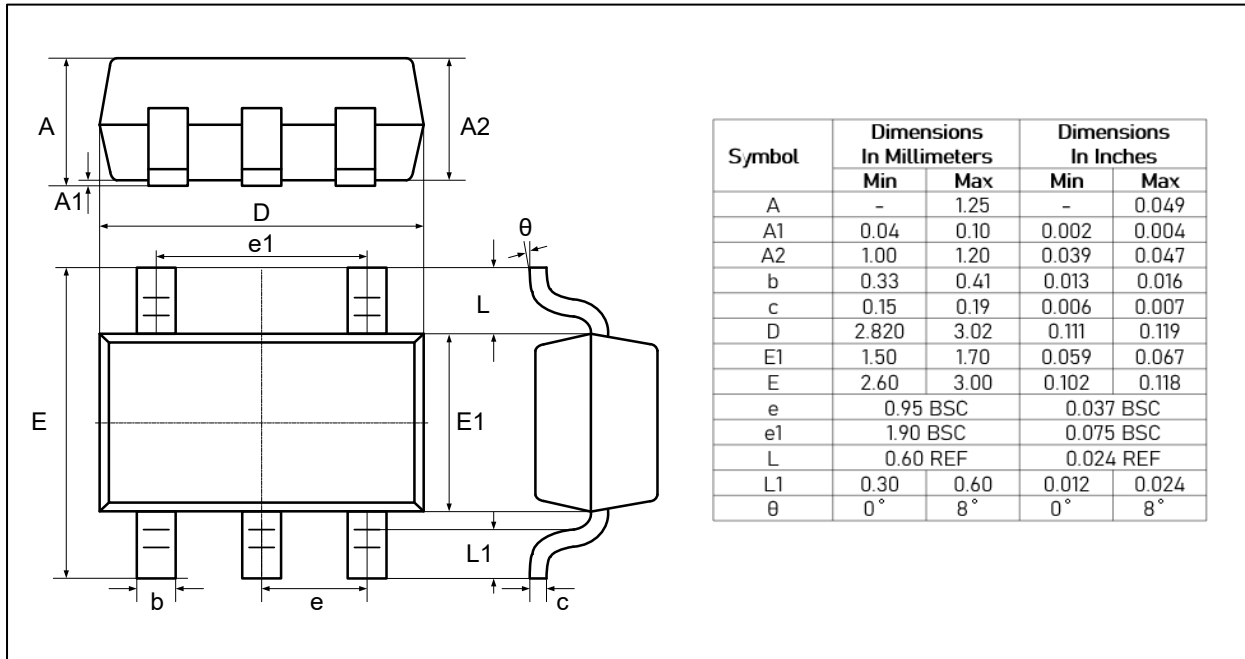


* All dimensions are nominal

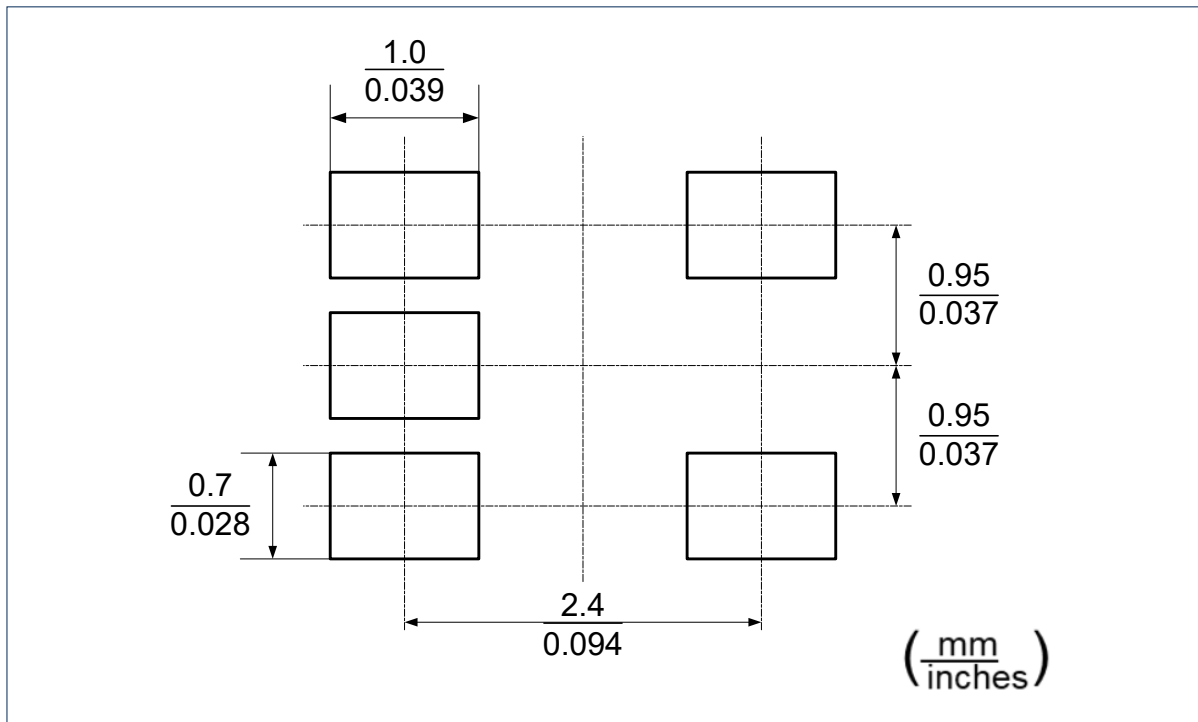
Device	Package Type	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
LTA8191XT5/R6	SOT23	5	3 000	178	9.0	3.3	3.2	1.5	4.0	8.0	Q3
LTA8191XS8/R8	SOIC	8	4 000	330	12.5	6.6	5.3	2.0	8.0	12.0	Q1
LTA8191XV8/R6	MSOP	8	3 000	330	12.5	5.0	3.5	2.0	8.0	12.0	Q1
LTA8192XS8/R8	SOIC	8	4 000	330	12.5	6.6	5.3	2.0	8.0	12.0	Q1
LTA8192XV8/R6	MSOP	8	3 000	330	12.5	5.0	3.5	2.0	8.0	12.0	Q1

Package Outlines

DIMENSIONS, SOT23-5L

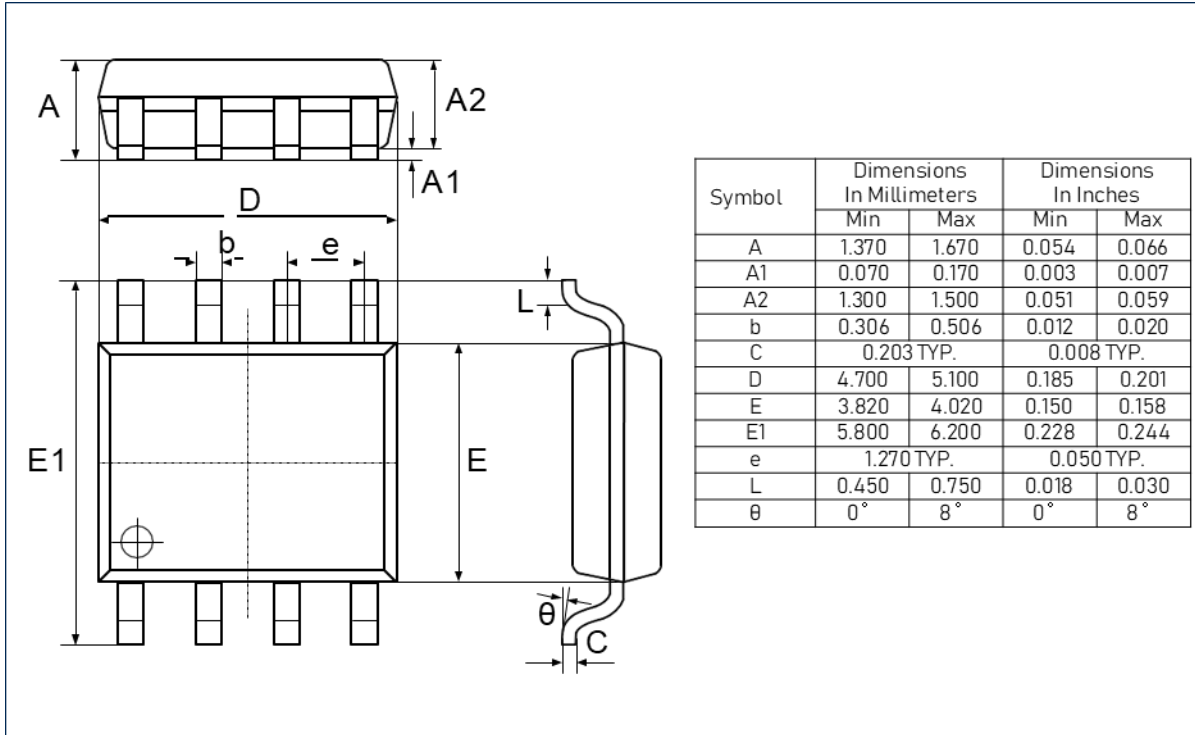


RECOMMENDED SOLDERING FOOTPRINT, SOT23-5L

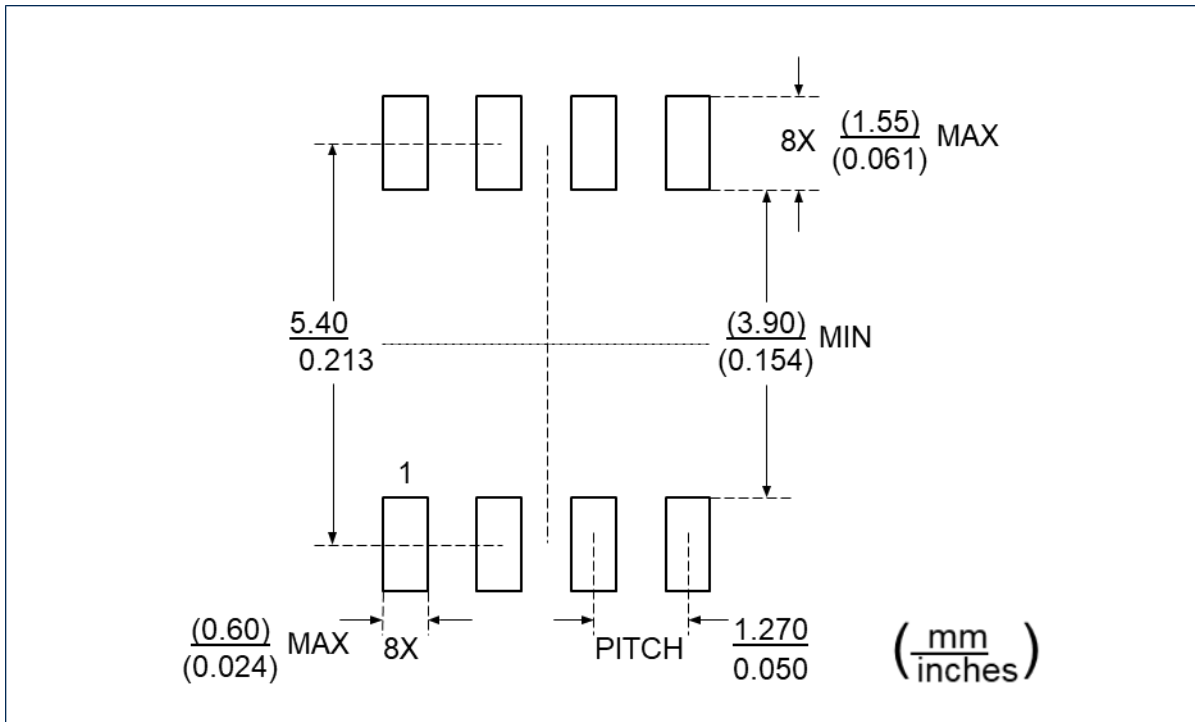


Package Outlines (Cont.)

DIMENSIONS, SOIC-8L

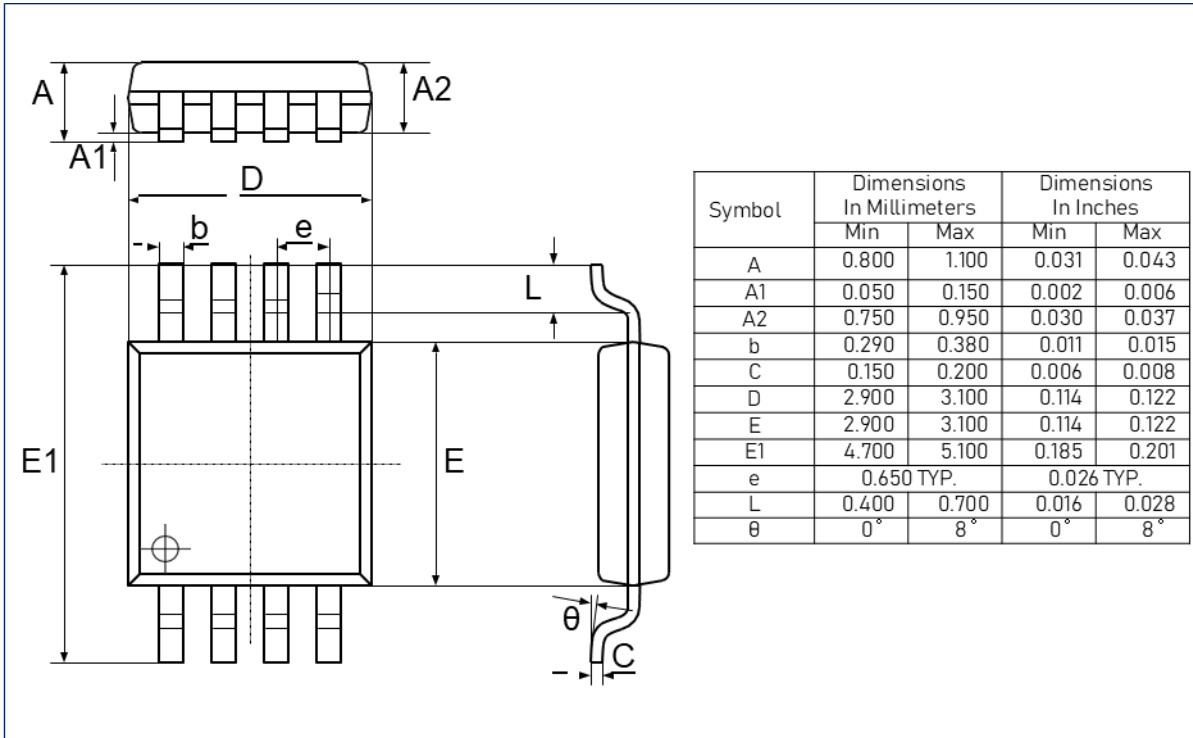


RECOMMENDED SOLDERING FOOTPRINT, SOIC-8L

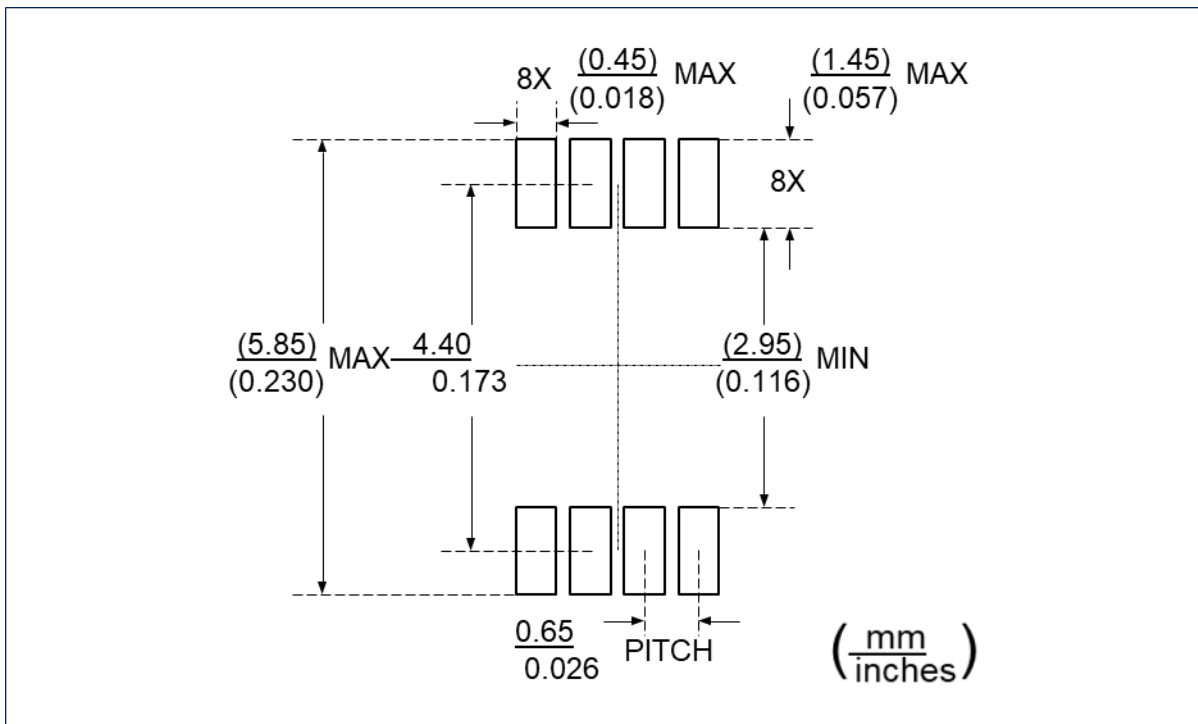


Package Outlines (Cont.)

DIMENSIONS, MSOP-8L



RECOMMENDED SOLDERING FOOTPRINT, MSOP-8L



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Important Notice

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