

General Description

The LTA8262Q is an automotive micro-power, 42V wide supply voltage, rail-to-rail output operational amplifier capable of operating on supplies ranging from +4.5V to +42V.

The LTA8262Q device offer outstanding dc precision and ac performance, including low offset (± 4 mV maximum), low offset drift (± 2 $\mu\text{V}/^\circ\text{C}$ typically), 1.1 MHz bandwidth, and 22 nV/ $\sqrt{\text{Hz}}$ input voltage noise density at 1 kHz. Unique features such as differential input-voltage range to the negative supply rail, high output current (± 45 mA), high capacitive load drive of up to 1 nF, and high slew rate (0.45V/ μs) make the LTA8262Q high performance operational amplifiers performance for high-voltage industrial applications.

The robust design of the LTA8262Q provide ease-of-use to the circuit designer: integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection. The LTA8262Q is optimized for operation at voltages from +4.5V ($\pm 2.25\text{V}$) to +42V ($\pm 21\text{V}$) over the extended temperature range of -40°C to $+125^\circ\text{C}$.

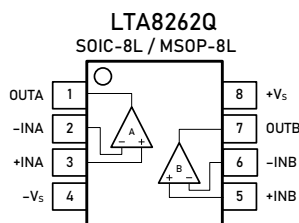
Features and Benefits

- Wide Supply: ± 2.25 V to $\pm 21\text{V}$, 4.5 V to 42V
- Low Offset Voltage: ± 4 mV Maximum
- Low Offset Voltage Drift: ± 2 $\mu\text{V}/^\circ\text{C}$
- High Common-Mode Rejection: 112 dB
- Gain Bandwidth: 1.1 MHz
- Slew Rate: 0.45 V/ μs
- Low Noise: 22 nV/ $\sqrt{\text{Hz}}$ at 1 kHz
- Low Quiescent Current: 135 μA per amplifier
- Low Bias Current: ± 10 pA
- Rail-to-Rail Output
- Optimized for AEC-Q100 grade 1 applications

Applications

- Automotive headlighting
- On-Board Charger(OBC)
- Body Electronics
- Inverter and motor control
- Battery management system(BMS)

Pin Configuration (Top View)



Pin Description

Symbol	Description
-IN	Inverting input of the amplifier. The voltage range is from V_{S-} to $V_{S+} - 1.5\text{ V}$.
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as -IN.
+V _S	Positive power supply. The voltage is from 4.5 V to 42V. Split supplies are possible as long as the voltage between V_{S+} and V_{S-} is from 4.5 V to 42V.
-V _S	Negative power supply. It is normally tied to ground. It can also be tied to a voltage other than ground as long as the voltage between V_{S+} and V_{S-} is from 4.5 V to 42V.
OUT	Amplifier output.

Ordering Information ⁽¹⁾

Type Number	Package Name	Package Quantity	Eco Class ⁽²⁾	Marking Code ⁽³⁾
LTA8262QS8/R8	SOIC-8L	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	HVQ62
LTA8262QV8/R6	MSOP-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	HVQ62

(1) Please contact to your Linearin representative for the latest availability information and product content details.

(2) Eco Class - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & Halogen Free).

(3) There may be multiple device markings, a varied marking character of "x", or additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

Limiting Value - In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Absolute Maximum Rating
Supply Voltage, V_{S+} to V_{S-}	48V
Signal Input Terminals: Voltage, Current	$-V_S - 0.3\text{ V}$ to $+V_S + 0.3\text{ V}$, $\pm 10\text{ mA}$
Output Short-Circuit	Continuous
Storage Temperature Range, T_{stg}	-65 to $+150\text{ }^{\circ}\text{C}$
Junction Temperature, T_J	$150\text{ }^{\circ}\text{C}$
Lead Temperature Range (Soldering 10 sec)	$260\text{ }^{\circ}\text{C}$

ESD Rating

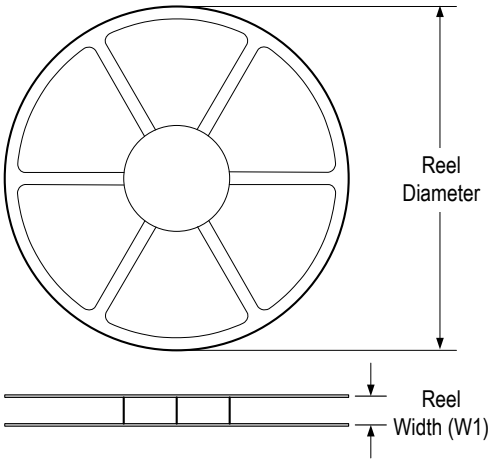
Parameter	Item	Value	Unit
Electrostatic Discharge Voltage	Human body model (HBM), per MIL-STD-883J / Method 3015.9 ⁽¹⁾	2000	V
	Charged device model (CDM), per ESDA/JEDEC JS-002-2014 ⁽²⁾	2000	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 500-V HBM is possible if necessary precautions are taken.

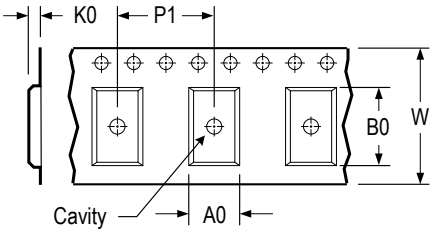
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 250-V CDM is possible if necessary precautions are taken.

Tape and Reel Information

REEL DIMENSIONS

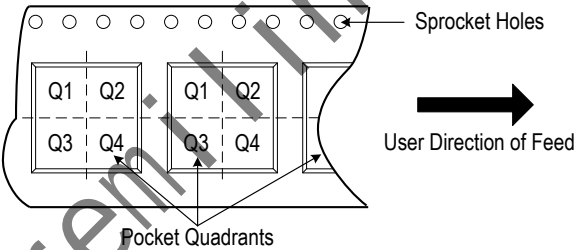


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

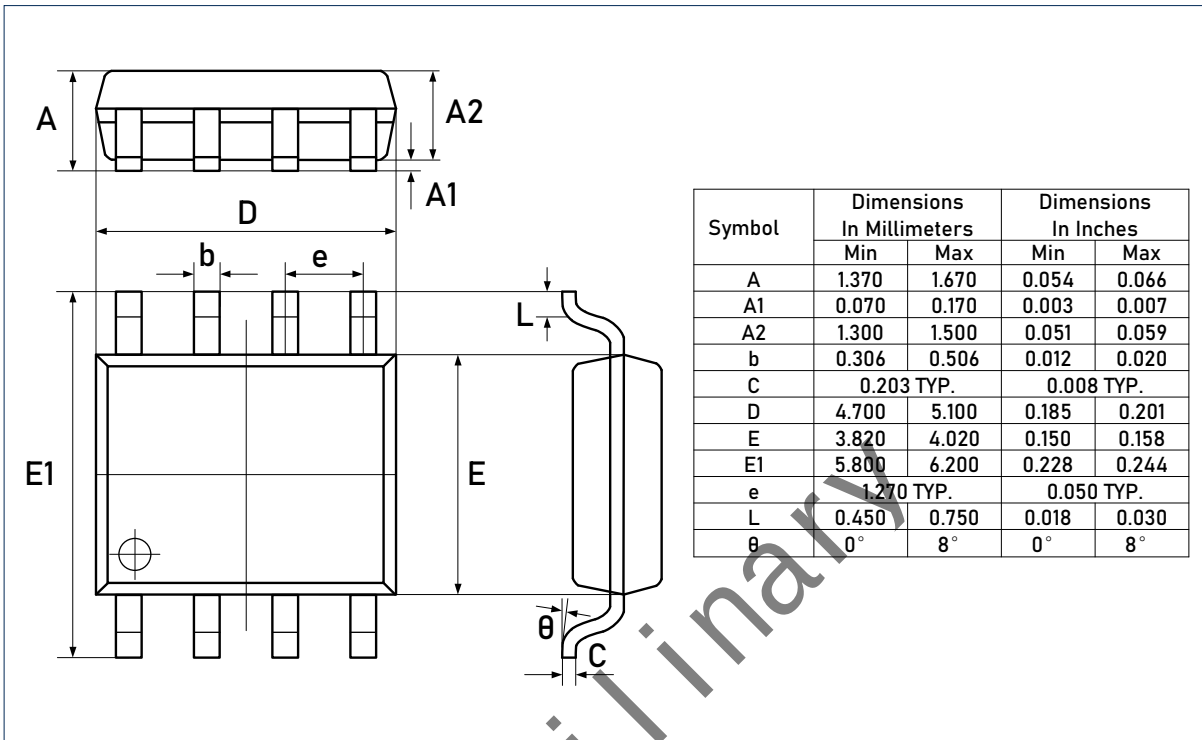


* All dimensions are nominal

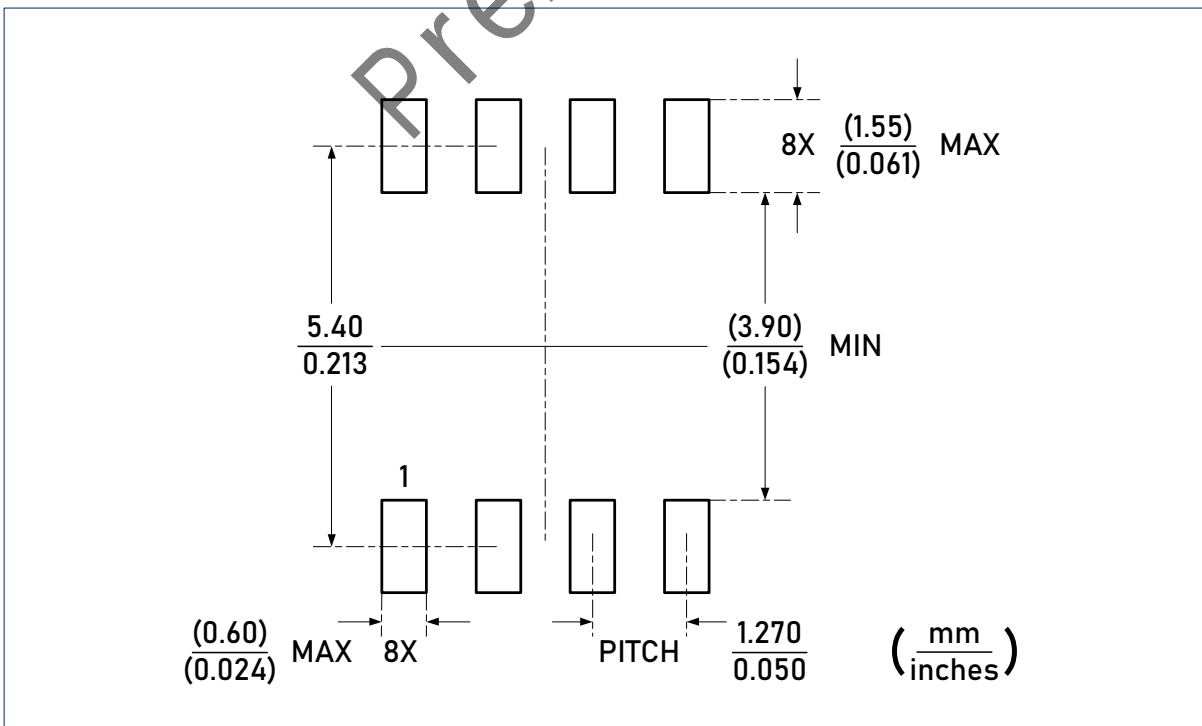
Device	Package Type	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
LTA8262QV8/R6	MSOP8	8	3 000	330	12.4	5.0	3.5	2.0	8.0	12.0	Q1
LTA8262QS8/R6	SOP8	8	4 000	330	12.4	6.6	5.3	2.0	8.0	12.0	Q1

Package Outlines (continued)

DIMENSIONS, SOIC-8L

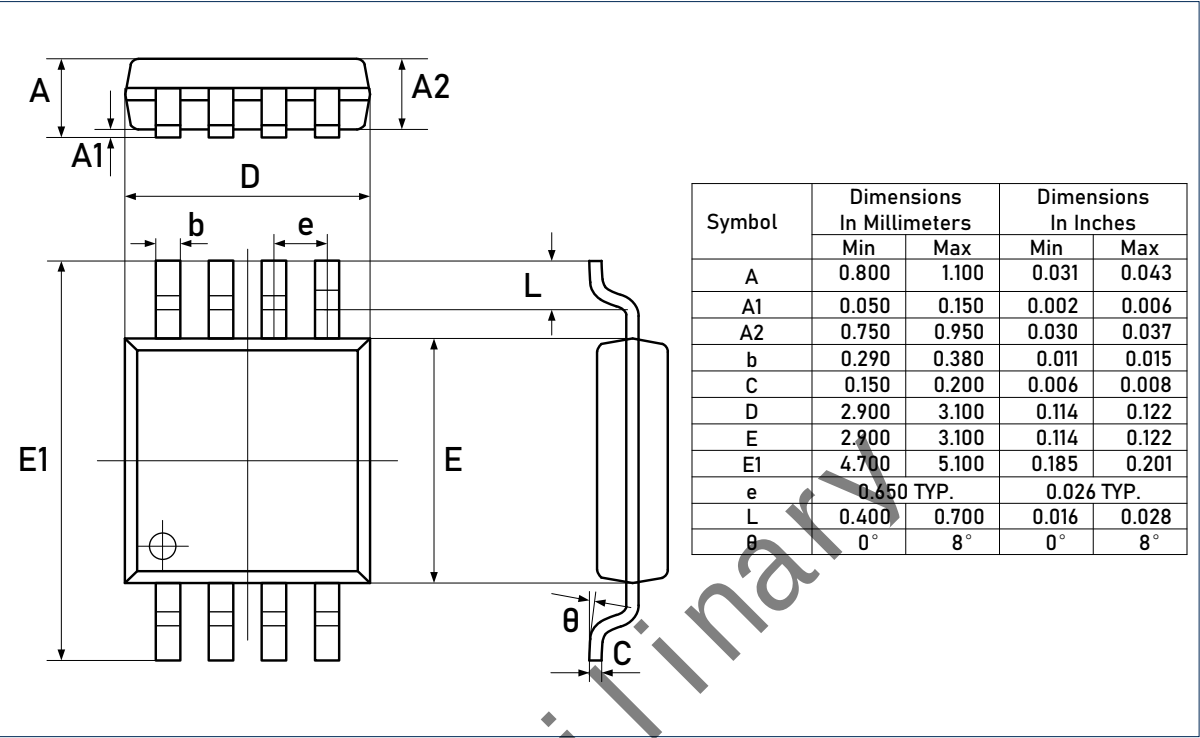


RECOMMENDED SOLDERING FOOTPRINT, SOIC-8L

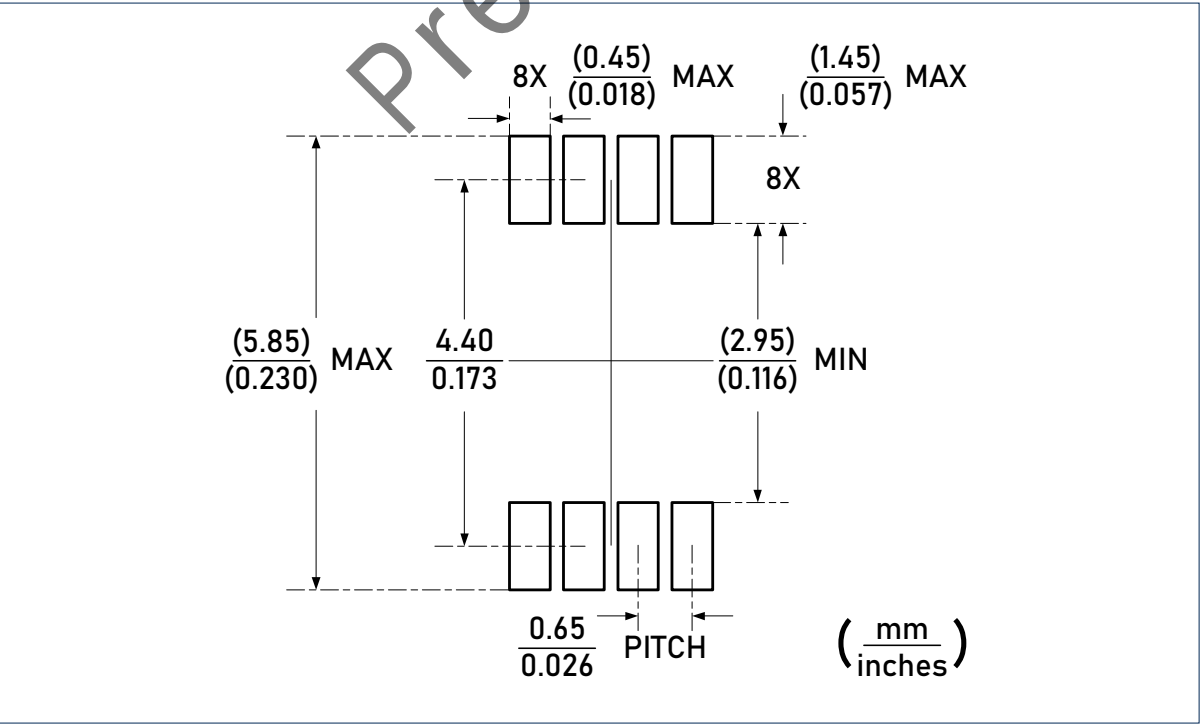


Package Outlines (continued)

DIMENSIONS, MSOP-8L



RECOMMENDED SOLDERING FOOTPRINT, MSOP-8L



Important Notice

Linearin is a global fabless semiconductor company specializing in advanced high-performance high-quality analog/mixed-signal IC products and sensor solutions. The company is devoted to the innovation of high performance, analog-intensive sensor front-end products and modular sensor solutions, applied in multi-market of medical & wearable devices, smart home, sensing of IoT, intelligent industrial & smart factory (industrie 4.0), and automotives. Linearin's product families include widely-used standard catalog products, solution-based application specific standard products (ASSPs) and sensor modules that help customers achieve faster time-to-market products. Go to <http://www.linearin.com> for a complete list of Linearin product families.

For additional product information, or full datasheet, please contact with the Linearin's Sales Department or Representatives.

Preliminary