

General Description

The LTC8635Q is a single channel operational amplifier, representing a new generation of general-purpose, low-power operational amplifiers. Featuring rail-to-rail input and output (RRIO) swings, low quiescent current (typical 750 μ A) combined with a wide bandwidth (11 MHz) makes this family very attractive for a variety of battery-powered applications that require a good balance between cost and performance, such as audio outputs, motor phase current sensing, photodiode amplification, barcode scanners and white goods. The low input bias current supports these amplifiers to be used in applications with mega-ohm source impedances.

The robust design of the LTC8635Q amplifiers provides ease-of-use to the circuit designer: unity-gain stability with capacitive loads of up to 300 pF, integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection. The LTC8635Q amplifiers are optimized for operation at voltages as low as +2.0 V (± 1.0 V) to +5.5 V (± 2.75 V) over the extended temperature range of -40°C to $+125^{\circ}\text{C}$.

The LTC8635Q is available in SOT23-5L.

Features and Benefits

- Low Input Offset Voltage: ± 0.35 mV Maximum
- Wide Unity-Gain Bandwidth: 11 MHz
- High Slew Rate: 11 V/ μ s
- Fast Settling: 0.26 μ s to 0.1%
- Rail-to-Rail Input and Output
- Internal RF/EMI Filter
- Single 2 V to 5.5 V Supply Voltage Range
- Low Supply Current: 750 μ A at 5V Supply Per Amplifier
- Extended Temperature Range: -40°C to $+125^{\circ}\text{C}$
- Optimized for AEC-Q100 grade 1 applications

Applications

- Automotive headlighting
- On-Board Charger (OBC)
- Body Electronics
- Inverter and motor control
- Battery management system (BMS)

Pin Configurations (Top View)

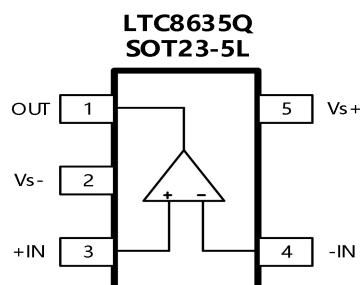


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Pin Description

Symbol	Description
-IN	Inverting input of the amplifier. The voltage range is from ($V_{S-} - 0.1V$) to ($V_{S+} + 0.1V$).
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as -IN.
V_{S+}	Positive power supply.
V_{S-}	Negative power supply.
OUT	Amplifier output.

Ordering Information

Type Number	Package Name	Package Quantity	Marking Code
LTC8635QT5/R6	SOT23-5L	Tape and Reel, 3 000	Q35

Limiting Value

Parameter	Absolute Maximum Rating
Supply Voltage, V_{S+} to V_{S-}	10.0 V
Signal Input Terminals: Voltage, Current	$V_{S-} - 0.5 V$ to $V_{S+} + 0.5 V$, ± 10 mA
Output Short-Circuit	Continuous
Storage Temperature Range, T_{stg}	$-65^{\circ}C$ to $+150^{\circ}C$
Junction Temperature, T_j	$150^{\circ}C$
Lead Temperature Range (Soldering 10 sec)	$260^{\circ}C$

ESD Rating

Parameter	Item	Value	Unit
Electrostatic Discharge Voltage	Human body model (HBM), per AEC-Q100-002	$\pm 2\ 000$	V
	Charged device model (CDM), per AEC-Q100-011	$\pm 2\ 000$	

Electrical Characteristics

$V_S = 5.0V$, $T_A = +25^\circ C$, $V_{CM} = V_S/2$, $V_O = V_S/2$, and $R_L = 10k\Omega$ connected to $V_S/2$, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
OFFSET VOLTAGE						
V _{OS}	Input offset voltage			± 0.10	± 0.35	mV
V _{OS TC}	Offset voltage drift	T _A = −40 to +125 °C		±1	2.3	μV/°C
PSRR	Power supply rejection ratio	V _S = 2.0 to 5.5 V, V _{CM} < V _{S+} − 2V	95	110		dB
		T _A = −40 to +125 °C	82			
INPUT BIAS CURRENT						
I _B	Input bias current			1		pA
		T _A = +85 °C		150		
		T _A = +125 °C		500		
I _{OS}	Input offset current			1		pA
NOISE						
V _n	Input voltage noise	f = 0.1 to 10 Hz		10		μV _{P-P}
e _n	Input voltage noise density	f = 10 kHz		20		nV/√Hz
I _n	Input current noise density	f = 1 kHz		3		fA/√Hz
INPUT VOLTAGE						
V _{CM}	Common-mode voltage range		V _{S−} −0.1		V _{S+} +0.1	V
CMRR	Common-mode rejection ratio	V _S = 5.5 V, V _{CM} = −0.1 to 5.6 V	80	96		dB
		V _{CM} = 0 to 5.3 V, T _A = −40 to +125 °C	75			
		V _S = 2.0 V, V _{CM} = −0.1 to 2.1 V	76	90		
		V _{CM} = 0 to 1.8 V, T _A = −40 to +125 °C	70			
INPUT IMPEDANCE						
C _{IN}	Input capacitance	Differential		2.0		pF
		Common mode		3.5		
OPEN-LOOP GAIN						
A _{VOL}	Open-loop voltage gain	R _L = 10 kΩ, V _O = 0.05 to 3.5 V	97	105		dB
		T _A = −40 to +125 °C	87			
		R _L = 600 Ω, V _O = 0.15 to 3.5 V	85	90		
		T _A = −40 to +125 °C	75			
FREQUENCY RESPONSE						
GBW	Gain bandwidth product			11		MHz
SR	Slew rate	G = +1, C _L = 100 pF, V _O = 1.5 to 3.5 V		11		V/μs
THD+N	Total harmonic distortion + noise	G = +1, f = 1 kHz, V _O = 0.5 V _{RMS}		0.0005		%
t _S	Settling time	To 0.1%, G = +1, 1V step		0.26		μs
		To 0.01%, G = +1, 1V step		0.34		
t _{OR}	Overload recovery time	V _{IN} * Gain > V _S		0.3		μs

CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures.

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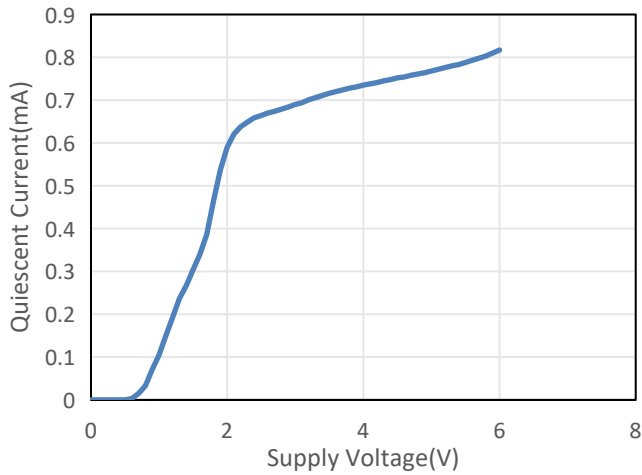
Electrical Characteristics (continued)

$V_S = 5.0V$, $T_A = +25^{\circ}C$, $V_{CM} = V_S/2$, $V_O = V_S/2$, and $R_L = 10k\Omega$ connected to $V_S/2$, unless otherwise noted.

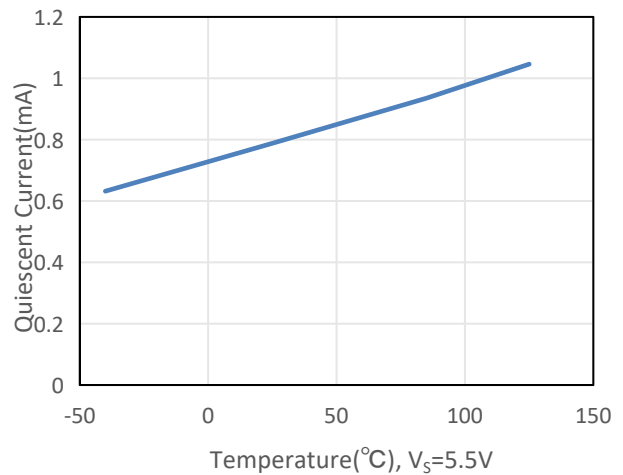
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
OUTPUT						
V _{OH}	High output voltage swing	R _L = 10 kΩ	V _{S+} -12	V _{S+} -8		mV
		R _L = 600 Ω	V _{S+} -180	V _{S+} -125		
V _{OL}	Low output voltage swing	R _L = 10 kΩ		V _{S-} +6	V _{S-} +9	mV
		R _L = 600 Ω		V _{S-} +90	V _{S-} +130	
I _{SC}	Short-circuit current			± 75		mA
POWER SUPPLY						
V _S	Operating supply voltage	T _A = -40 to +125 °C	2.0		5.5	V
I _Q	Quiescent current (per amplifier)	V _S = 2.0 V		625	760	μA
		V _S = 5.0 V		750	930	
THERMAL CHARACTERISTICS						
T _A	Operating temperature range		-40		+125	°C
θ _{JA}	Package Thermal Resistance	SOT23-5L		190		°C/W

Typical Characteristics

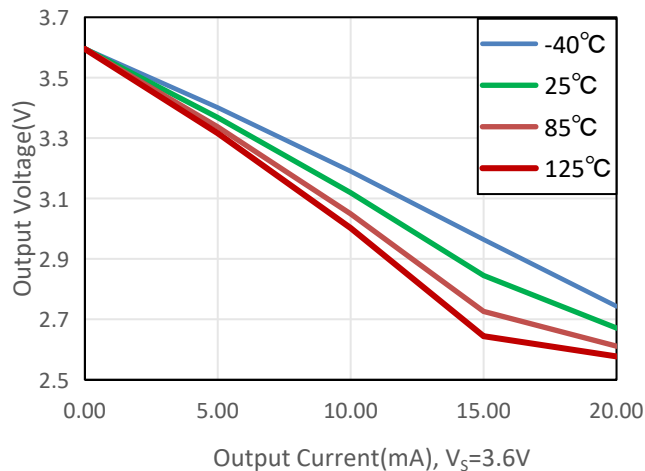
$V_S = 5.0V$, $T_A = +25^\circ C$, $V_{CM} = V_S/2$, $V_O = V_S/2$, and $R_L = 10k\Omega$ connected to $V_S/2$, unless otherwise noted.



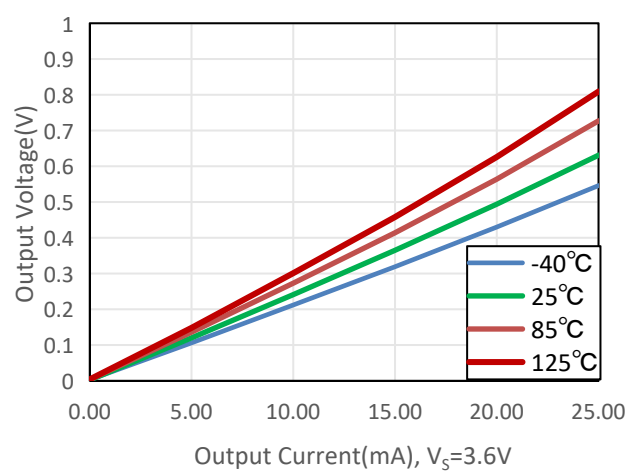
Quiescent Current as a function of Supply Voltage



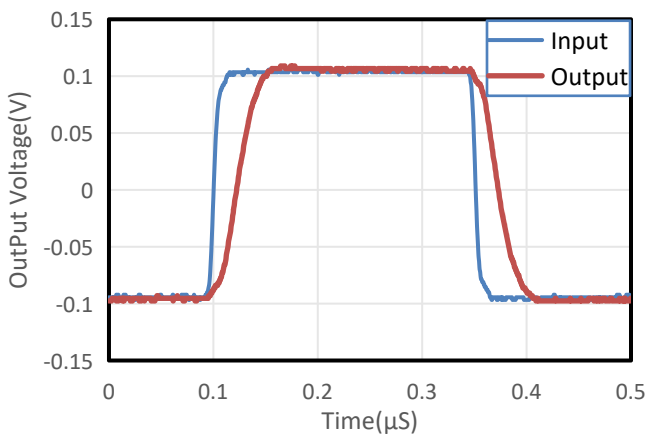
Quiescent Current as a function of Temperature



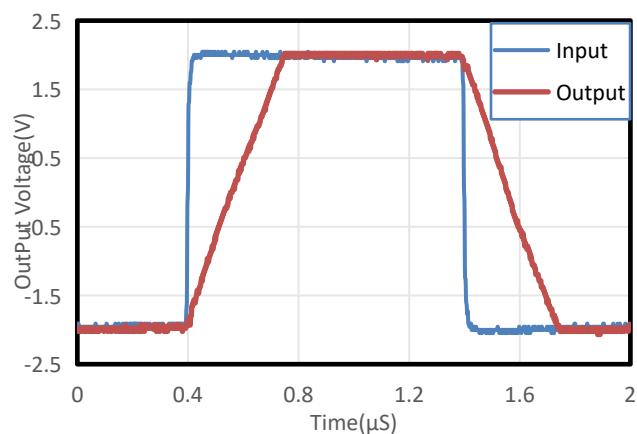
Output Voltage Swing as a function of Output Current (Sourcing, $V_S = 3.6V$)



Output Voltage Swing as a function of Output Current (Sinking, $V_S = 3.6V$)



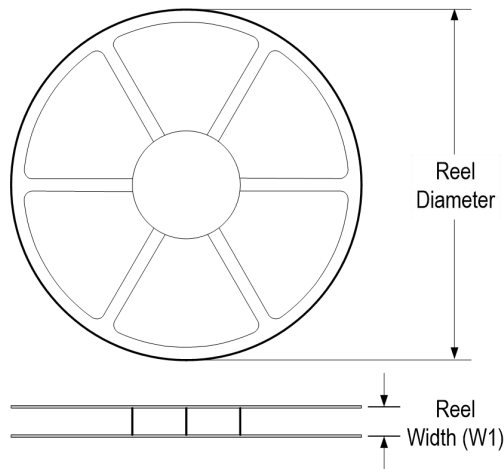
Small-Signal Step Response



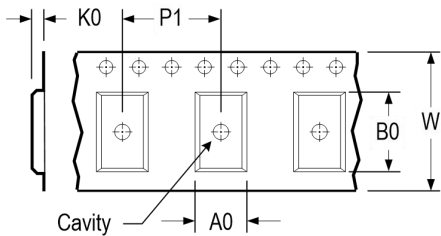
Large-Signal Step Response

Tape and Reel Information

REEL DIMENSIONS

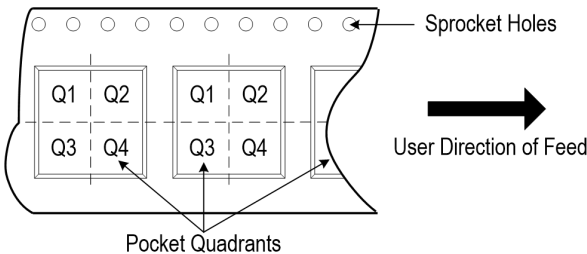


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

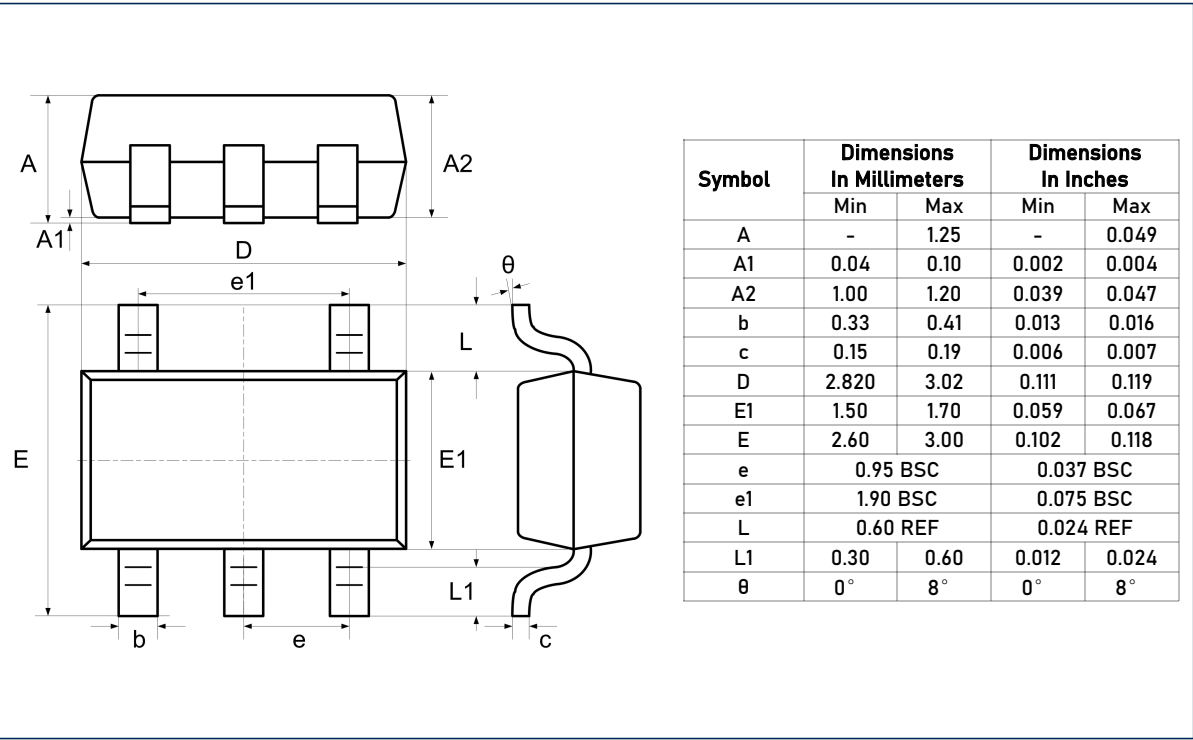


* All dimensions are nominal

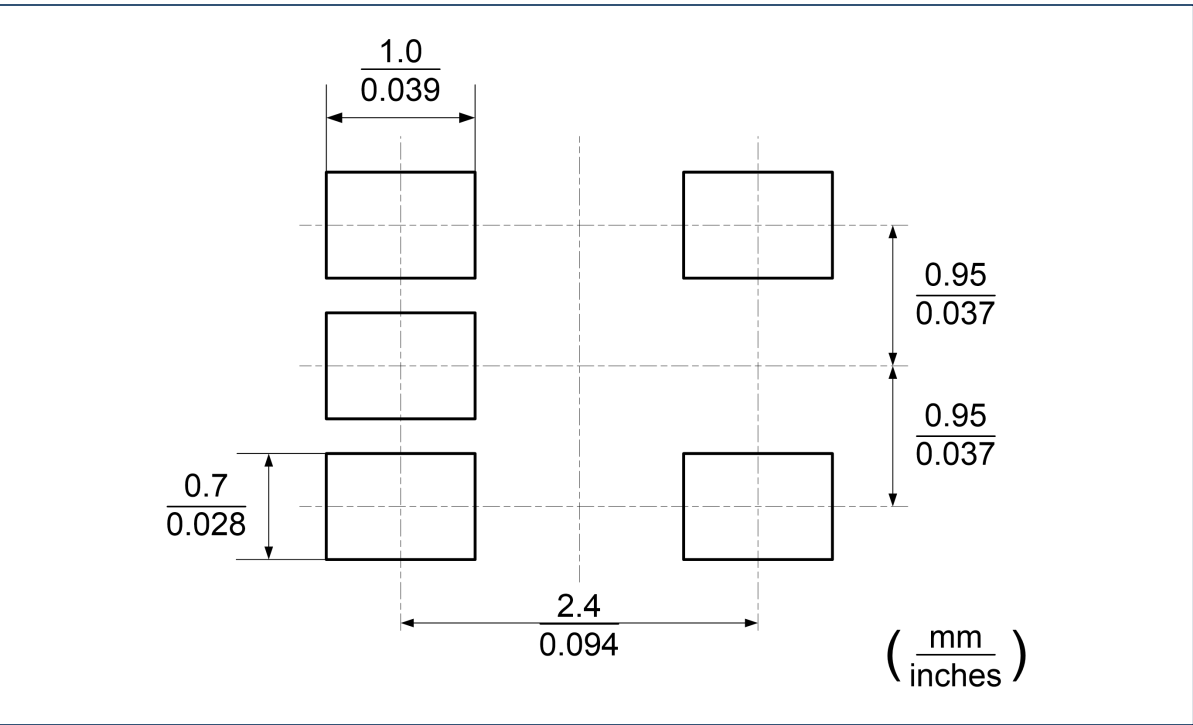
Device	Package Type	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
LTC8635QT5/R6	SOT23	5	3 000	178	9.5	3.3	3.2	1.5	4.0	8.0	Q3

Package Outlines

DIMENSIONS, SOT23-5L



RECOMMENDED SOLDERING FOOTPRINT, SOT23-5L



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Important Notice

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